

# EECS3342 Winter 2023

## Notes on Discharging PO of Deadlock Freedom (DLF)

### Bridge Controller: Initial Model

CHEN-WEI WANG

#### 1 Discharging the PO of Deadlock Freedom: First Attempt

$$\boxed{\begin{array}{l} d \in \mathbb{N} \\ n \in \mathbb{N} \\ n \leq d \\ \vdash \\ n < d \vee n > 0 \end{array}}$$

≡

$$\boxed{\begin{array}{l} d \in \mathbb{N} \\ n \in \mathbb{N} \\ n < d \vee n = d \\ \vdash \\ n < d \vee n > 0 \end{array}}$$

**MON**

$$\boxed{\begin{array}{l} n < d \vee n = d \\ \vdash \\ n < d \vee n > 0 \end{array}} \text{ ORL } \left\{ \begin{array}{l} \boxed{\begin{array}{l} n < d \\ \vdash \\ n < d \vee n > 0 \end{array}} \text{ OR\_R1 } \boxed{\begin{array}{l} n < d \\ \vdash \\ n < d \end{array}} \text{ HYP} \\ \boxed{\begin{array}{l} n = d \\ \vdash \\ n < d \vee n > 0 \end{array}} \text{ EQ\_LR } \boxed{\begin{array}{l} n = d \\ \vdash \\ d < d \vee d > 0 \end{array}} \text{ MON } \boxed{\begin{array}{l} \vdash \\ d < d \vee d > 0 \end{array}} \text{ OR\_R2 } \boxed{\begin{array}{l} \vdash \\ d > 0 \end{array}} ? \end{array} \right.$$

## 2 Discharging the PO of Deadlock Freedom: Second Attempt (after adding axm0.2)

$$\begin{array}{l} d \in \mathbb{N} \\ d > 0 \\ n \in \mathbb{N} \\ n \leq d \\ \vdash \\ n < d \vee n > 0 \end{array}$$
 $\equiv$ 

$$\begin{array}{l} d \in \mathbb{N} \\ d > 0 \\ n \in \mathbb{N} \\ n < d \vee n = d \\ \vdash \\ n < d \vee n > 0 \end{array}$$

**MON**

