Specifying & Refining a Bridge Controller

MEB: Chapter 2



EECS3342 Z: System Specification and Refinement Winter 2022

CHEN-WEI WANG

Learning Outcomes



This module is designed to help you understand:

- What a Requirement Document (RD) is
- What a refinement is
- Writing formal specifications
 - (Static) contexts: constants, axioms, theorems
 - (Dynamic) machines: variables, invariants, events, guards, actions
- Proof Obligations (POs) associated with proving:
 - o refinements
 - system properties
- Applying *inference rules* of the *sequent calculus*

2 of 124

Recall: Correct by Construction



- Directly reasoning about source code (written in a programming) language) is too complicated to be feasible.
- Instead, given a requirements document, prior to implementation, we develop *models* through a series of *refinement* steps:
 - Each model formalizes an external observer's perception of the system.
 - Models are "sorted" with increasing levels of accuracy w.r.t. the system.
 - The first model, though the most abstract, can already be proved satisfying some *requirements*.
 - Starting from the second model, each model is analyzed and proved correct relative to two criteria:
 - 1. Some *requirements* (i.e., R-descriptions)
 - 2. Proof Obligations (POs) related to the preceding model being refined by the current model (via "extra" state variables and events).
 - The *last model* (which is *correct by construction*) should be sufficiently close to be transformed into a working program (e.g., in C).

3 of 124

State Space of a Model



- A model's state space is the set of all configurations:
 - Each configuration assigns values to constants & variables, subject to:
 - axiom (e.g., typing constraints, assumptions)
 - *invariant* properties/theorems
 - Say an initial model of a bank system with two constants and a variable:

```
c \in \mathbb{N}1 \land L \in \mathbb{N}1 \land accounts \in String \Rightarrow \mathbb{Z}
                                                                                      /* typing constraint */
\forall id \bullet id \in dom(accounts) \Rightarrow -c \leq accounts(id) \leq L /* desired property */
```

- **Q**. What is the **state space** of this initial model?
- **A**. All valid combinations of *c*, *L*, and *accounts*.
- Configuration 1: $(c = 1,000, L = 500,000, b = \emptyset)$
- Configuration 2: $(c = 2,375, L = 700,000, b = \{("id1",500), ("id2",1,250)\})$ [Challenge: Combinatorial Explosion]

- Model Concreteness ↑ ⇒ (State Space ↑ ∧ Verification Difficulty ↑)
- A model's *complexity* should be guided by those properties intended to be verified against that model.
 - ⇒ *Infeasible* to prove all desired properties on a model.
 - ⇒ *Feasible* to distribute desired properties over a list of *refinements*.

Roadmap of this Module



 We will walk through the development process of constructing models of a control system regulating cars on a bridge.

Such controllers exemplify a *reactive system*.

(with **sensors** and **actuators**)

- Always stay on top of the following roadmap:
 - 1. A Requirements Document (RD) of the bridge controller
 - 2. A brief overview of the *refinement strategy*
 - 3. An initial, the most abstract model
 - 4. A subsequent *model* representing the 1st refinement
 - 5. A subsequent *model* representing the 2nd refinement
 - 6. A subsequent *model* representing the 3rd refinement

5 of 124



Requirements Document: Mainland, Island

Imagine you are asked to build a bridge (as an alternative to ferry) connecting the downtown and Toronto Island.



Page Source: https://soldbyshane.com/area/toronto-islands/

Requirements Document: E-Descriptions



Each *E-Description* is an <u>atomic</u> <u>specification</u> of a <u>constraint</u> or an <u>assumption</u> of the system's working environment.

ENV1	ENV1 The system is equipped with two traffic lights with two colors: green and red.					
ENV2	The traffic lights control the entrance to the bridge at both ends of it.					
ENV3	Cars are not supposed to pass on a red traffic light, only on a green one.					
ENV4	The system is equipped with four sensors with two states: on or off.					
ENV5	The sensors are used to detect the presence of a car entering or leaving the bridge: "on" means that a car is willing to enter the bridge or to leave it.					

7 of 124

Requirements Document: R-Descriptions



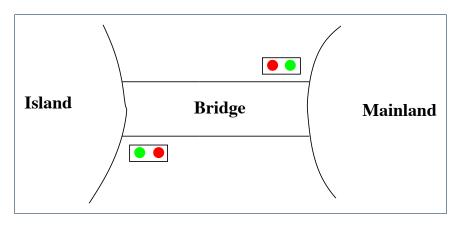
Each *R-Description* is an <u>atomic</u> *specification* of an intended *functionality* or a desired *property* of the working system.

REQ1	The system is controlling cars on a bridge connecting the mainland to an island.
REQ2	The number of cars on bridge and island is limited.
REQ3	The bridge is one-way or the other, not both at the same time.





Requirements Document: Visual Summary of Equipment Pieces



9 of 124

Refinement Strategy



- Before diving into details of the models, we first clarify the adopted design strategy of progressive refinements.
 - The <u>initial model</u> (m₀) will address the intended functionality of a limited number of cars on the island and bridge.

[REQ2]

 A 1st refinement (m₁ which refines m₀) will address the intended functionality of the bridge being one-way.

[REQ1, REQ3]

 A 2nd refinement (m₂ which refines m₁) will address the environment constraints imposed by traffic lights.

[ENV1, ENV2, ENV3]

 A <u>final</u>, 3rd refinement (m₃ which refines m₂) will address the environment constraints imposed by <u>sensors</u> and the <u>architecture</u>: controller, environment, communication channels.

[ENV4, ENV5]

• Recall *Correct by Construction*:

From each *model* to its *refinement*, only a <u>manageable</u> amount of details are added, making it *feasible* to conduct **analysis** and **proofs**.

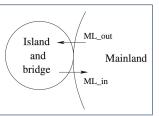
Model m_0 : Abstraction



- In this <u>most</u> abstract perception of the bridge controller, we do <u>not</u> even consider the bridge, traffic lights, and sensors!
- Instead, we focus on this single requirement:

REQ2 The number of cars on bridge and island is limited.

- Analogies:
 - Observe the system from the sky: island and bridge appear only as a compound.



"Zoom in" on the system as refinements are introduced.

11 of 124

Model m_0 : State Space



1. The *static* part is fixed and may be seen/imported.

A *constant d* denotes the <u>maximum</u> number of cars allowed to be on the *island-bridge compound* at any time.

(whereas cars on the mainland is unbounded)

constants: d axioms: $axm0_1: d \in \mathbb{N}$

Remark. Axioms are assumed true and may be used to prove theorems.

2. The *dynamic* part changes as the system *evolves*.

A *variable n* denotes the actual number of cars, at a given moment, in the *island-bridge compound*.



Remark. Invariants should be (subject to proofs):

- **Established** when the system is first initialized
- Preserved/Maintained after any enabled event's actions take effect

12 of 124



Model m_0 : State Transitions via Events

- The system acts as an ABSTRACT STATE MACHINE (ASM): it evolves as actions of enabled events change values of variables, subject to invariants.
- At any given **state** (a valid **configuration** of constants/variables):
 - An event is said to be *enabled* if its guard evaluates to *true*.
 - An event is said to be disabled if its guard evaluates to false.
 - An enabled event makes a state transition if it occurs and its actions take effect.
- 1st event: A car exits mainland (and enters the island-bridge compound).



Correct Specification? Say d = 2. Witness: Event Trace (init, ML_i n)

• 2nd event: A car enters mainland (and exits the island-bridge compound).

```
ML_in

begin

n:= n - 1

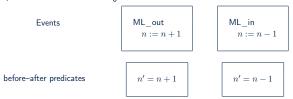
end
```

13 of 124

Correct Specification? Say d = 2. Witness: Event Trace (init, ML_out, ML_out, ML_out)

Model m_0 : Actions vs. Before-After Predicates on DE

- When an enabled event e occurs there are two notions of state:
 - o Before-/Pre-State: Configuration just before e's actions take effect
 - After-/Post-State: Configuration just after e's actions take effect
 Remark. When an enabled event occurs, its action(s) cause a transition from the pre-state to the post-state.
- As examples, consider actions of m₀'s two events:



- An event action "n:= n+1" is not a variable assignment; instead, it is a specification: "n becomes n+1 (when the state transition completes)".
- The before-after predicate (BAP) "n' = n + 1" expresses that
 n' (the post-state value of n) is one more than n (the pre-state value of n).
- When we express *proof obligations* (*POs*) associated with *events*, we use *BAP*.

Design of Events: Invariant Preservation



· Our design of the two events



only specifies how the *variable n* should be updated.

Remember, invariants are conditions that should never be violated!



By simulating the system as an ASM, we discover witnesses
 (i.e., event traces) of the invariants not being preserved all the time.

$$\exists s \bullet s \in \mathsf{STATE} \; \mathsf{SPACE} \Rightarrow \neg invariants(s)$$

 We formulate such a commitment to preserving invariants as a proof obligation (PO) rule (a.k.a. a verification condition (VC) rule).

15 of 124

Sequents: Syntax and Semantics



• We formulate each **PO/VC** rule as a (horizontal or vertical) **sequent**:

$$H \vdash G$$
 G

- The symbol ⊢ is called the *turnstile*.
- *H* is a <u>set</u> of predicates forming the *hypotheses*/*assumptions*.

[assumed as true]

• *G* is a set of predicates forming the *goal/conclusion*.

[claimed to be **provable** from H]

Informally:

 $H \vdash G$ is *true* if G can be proved by assuming H.

[i.e., We say "H entails G" or "H yields G"]

- \circ $H \vdash G$ is *false* if G cannot be proved by assuming H.
- Formally: $H \vdash G \iff (H \Rightarrow G)$
 - **Q**. What does it mean when H is empty (i.e., no hypotheses)?





INV

PO of Invariant Preservation: Sketch

Here is a sketch of the PO/VC rule for invariant preservation:

Axioms

Invariants Satisfied at Pre-State

Guards of the Event

Invariants Satisfied at Post-State

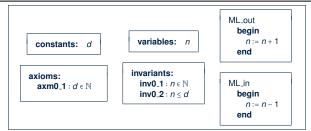
Informally, this is what the above PO/VC requires to prove:
 Assuming all axioms, invariants, and the event's guards hold at the pre-state, after the state transition is made by the event,

all invariants hold at the post-state.

17 of 124







- c: list of constants
- A(c): list of axioms
- v and v': list of variables in pre- and post-states
- $\langle axm0_{-1} \rangle$ $v \cong \langle n \rangle, v' \cong \langle n' \rangle$ $\langle inv0_{-1}, inv0_{-2} \rangle$

- *I(c, v)*: list of *invariants*
- G(c, v): the **event**'s list of guards

 $G(\langle d \rangle, \langle n \rangle)$ of $ML_out \cong \langle true \rangle$, $G(\langle d \rangle, \langle n \rangle)$ of $ML_in \cong \langle true \rangle$

- E(c, v): effect of the **event**'s actions i.t.o. what variable values **become**
 - $E(\langle d \rangle, \langle n \rangle)$ of $ML_out \cong \langle n+1 \rangle$, $E(\langle d \rangle, \langle n \rangle)$ of $ML_out \cong \langle n-1 \rangle$
- v' = E(c, v): **before-after predicate** formalizing E's actions
 - BAP of ML_out : $\langle \mathbf{n'} \rangle = \langle \mathbf{n} + 1 \rangle$, BAP of ML_in : $\langle \mathbf{n'} \rangle = \langle \mathbf{n} 1 \rangle$

18 of 124

Rule of Invariant Preservation: Sequents



 Based on the components (c, A(c), v, I(c, v), E(c, v)), we are able to formally state the PO/VC Rule of Invariant Preservation:

- Accordingly, how many *sequents* to be proved? [# events × # invariants]
- We have two **sequents** generated for **event** $ML_{-}out$ of model m_0 :



Exercise. Write the **POs of invariant preservation** for event ML_in.

Before claiming that a *model* is *correct*, outstanding *sequents* associated with all *POs* must be proved/discharged.

19 of 124

Inference Rules: Syntax and Semantics



• An *inference rule (IR)* has the following form:

A L

Formally: $A \Rightarrow C$ is an axiom.

Informally: To prove *C*, it is sufficient to prove *A* instead.

Informally: *C* is the case, assuming that *A* is the case.

- L is a name label for referencing the *inference rule* in proofs.
- A is a set of sequents known as antecedents of rule L.
- C is a single sequent known as consequent of rule L.
- Let's consider *inference rules (IRs)* with two different flavours:



- IR **MON**: To prove H1, $H2 \vdash G$, it suffices to prove $H1 \vdash G$ instead.
- ∘ IR **P2**: $n \in \mathbb{N} \vdash n+1 \in \mathbb{N}$ is an *axiom*.

[proved automatically without further justifications]



Proof of Sequent: Steps and Structure

• To prove the following sequent (related to *invariant preservation*):

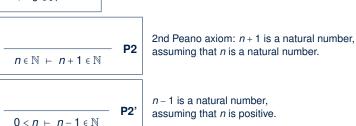
- 1. Apply a *inference rule*, which *transforms* some "outstanding" **sequent** to one or more other **sequents** to be proved instead.
- 2. Keep applying *inference rules* until <u>all</u> *transformed* sequents are *axioms* that do <u>not</u> require any further justifications.
- Here is a formal proof of ML_out/inv0_1/INV, by applying IRs MON and P2:

21 of 124

Example Inference Rules (1)







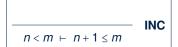
$$n \in \mathbb{N} \ \vdash \ 0 \le n$$

3rd Peano axiom: *n* is non-negative, assuming that *n* is a natural number.

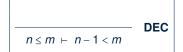
22 of 124

Example Inference Rules (2)





n+1 is less than or equal to m, assuming that n is strictly less than m.



n-1 is strictly less than m, assuming that n is less than or equal to m.

23 of 124

Example Inference Rules (3)



 $\frac{H1 \vdash G}{H1, H2 \vdash G} \quad MON$

To prove a goal under certain hypotheses, it suffices to prove it under less hypotheses.

$$\frac{H,P \vdash R \qquad H,Q \vdash R}{H,P \lor Q \vdash R} \quad \mathsf{OR_L}$$

<u>Proof by Cases</u>:
To prove a goal under a disjunctive assumption, it suffices to prove <u>independently</u> the same goal, <u>twice</u>, under each disjunct.

$$\frac{H \vdash P}{H \vdash P \lor Q} \quad \mathbf{OR} \mathbf{R} \mathbf{1}$$

To prove a disjunction, it suffices to prove the left disjunct.

$$\frac{H \vdash Q}{H \vdash P \lor Q} \quad \mathsf{OR_R2}$$

To prove a disjunction, it suffices to prove the right disjunct.



Revisiting Design of Events: *ML_out*

• Recall that we already proved **PO** ML_out/inv0_1/INV:

:. ML_out/inv0_1/INV succeeds in being discharged.

• How about the other **PO** ML_out/inv0_2/INV for the same event?

:. ML_out/inv0_2/INV fails to be discharged.

25 of 124



Revisiting Design of Events: ML_in

• How about the **PO** ML_in/inv0_1/INV for ML_in:

:. ML_in/inv0_1/INV fails to be discharged.

• How about the other **PO** ML_in/inv0_2/INV for the same event?

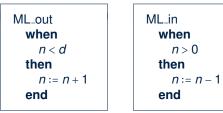
:. ML_in/inv0_2/INV succeeds in being discharged.

26 of 124

Fixing the Design of Events



- Proofs of ML_out/inv0_2/INV and ML_in/inv0_1/INV fail due to the two events being enabled when they should not.
- Having this feedback, we add proper *guards* to *ML_out* and *ML_in*:



- Having changed both events, <u>updated</u> **sequents** will be generated for the PO/VC rule of **invariant preservation**.
- <u>All sequents</u> ({ML_out, ML_in} × {inv0_1, inv0_2}) now provable?

27 of 124

Revisiting Fixed Design of Events: *ML_out*



• How about the **PO** ML_out/**inv0_1**/INV for ML_out:

- ∴ *ML_out/inv0_1/INV* still succeeds in being discharged!
- How about the other **PO** ML_out/inv0_2/INV for the same event?

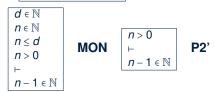


:. ML_out/inv0_2/INV now succeeds in being discharged!



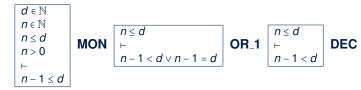
Revisiting Fixed Design of Events: ML_in

• How about the **PO** ML_in/inv0_1/INV for ML_in:



∴ *ML_in/inv0_1/INV* now succeeds in being discharged!

• How about the other **PO** ML_in/inv0_2/INV for the same event?



:. ML_in/inv0_2/INV still succeeds in being discharged!

29 of 124



Initializing the Abstract System m_0

- Discharging the four **sequents** proved that both **invariant** conditions are preserved between occurrences/interleavings of events ML_out and ML_in.
- But how are the invariants established in the first place?

Analogy. Proving *P* via *mathematical induction*, two cases to prove: \circ P(1), P(2), ...[base cases ≈ establishing inv.] $\circ P(n) \Rightarrow P(n+1)$ [inductive cases ≈ preserving inv.]

- Therefore, we specify how the **ASM**'s *initial state* looks like:
 - √ The IB compound, once initialized, has no cars.
 - ✓ Initialization always possible: guard is *true*.
 - √ There is no pre-state for init. begin : The RHS of := must not involve variables. n := 0: The RHS of := may only involve constants. √ There is only the post-state for init.
 - \therefore Before-After Predicate: n' = 0

PO of Invariant Establishment





- ✓ An reactive system, once initialized, should never terminate.
- ✓ Event init cannot "preserve" the invariants.
 - : State before its occurrence (*pre-state*) does not exist.
- ✓ Event *init* only required to *establish* invariants for the first time
- A new formal component is needed:
 - K(c): effect of *init*'s actions i.t.o. what variable values **become**

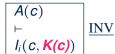
e.g.,
$$K(\langle d \rangle)$$
 of init $\widehat{=} \langle 0 \rangle$

• v' = K(c): **before-after predicate** formalizing *init*'s actions

e.g., BAP of *init*: $\langle n' \rangle = \langle 0 \rangle$

Accordingly, PO of invariant establisment is formulated as a sequent:





31 of 124

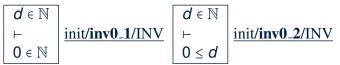
32 of 124

Discharging PO of Invariant Establishment LASSONDE



How many sequents to be proved?

- [# invariants]
- We have two **sequents** generated for **event** init of model m_0 :



• Can we discharge the **PO** init/inv0_1/INV ?



• Can we discharge the **PO** init/inv0_2/INV ?



30 of 124

init

end

LASSONDE SCHOOL OF ENGINEFERING

System Property: Deadlock Freedom

- So far we have proved that our initial model m₀ is s.t. <u>all</u> invariant conditions are:
 - o Established when system is first initialized via init
 - o Preserved whenevner there is a state transition

(via an enabled event: ML_out or ML_in)

- However, whenever <u>event occurrences</u> are <u>conditional</u> (i.e., <u>guards</u> stronger than <u>true</u>), there is a possibility of <u>deadlock</u>:
 - A state where guards of all events evaluate to false
 - When a *deadlock* happens, <u>none</u> of the *events* is *enabled*.
 - ⇒ The system is blocked and not reactive anymore!
- We express this *non-blocking* property as a new requirement:

REQ4	Once started, the system should work for ever.
------	--

33 of 124



PO of Deadlock Freedom (1)

• Recall some of the formal components we discussed:

 $G(\langle d \rangle, \langle n \rangle)$ of $ML_out \cong \langle n < d \rangle$, $G(\langle d \rangle, \langle n \rangle)$ of $ML_in \cong \langle n > 0 \rangle$

A system is deadlock-free if at least one of its events is enabled:

Axioms
Invariants Satisfied at Pre-State

DIF
Disjunction of the guards satisfied at Pre-State $\begin{array}{c}
A(c) \\
I(c, v) \\
F(G_1(c, v) \lor \cdots \lor G_m(c, v)
\end{array}$ DLF

To prove about deadlock freedom

- o An event's effect of state transition is **not** relevant.
- Instead, the evaluation of <u>all</u> events' guards at the pre-state is relevant.

34 of 124

PO of Deadlock Freedom (2)



- Deadlock freedom is not necessarily a desired property.
 - \Rightarrow When it is (like m_0), then the generated **sequents** must be discharged.
- Applying the PO of **deadlock freedom** to the initial model m_0 :

$$\begin{array}{c|c}
\hline
A(c) \\
I(c, \mathbf{v}) \\
\vdash \\
G_1(c, \mathbf{v}) \lor \cdots \lor G_m(c, \mathbf{v})
\end{array}$$

$$\underline{DLF} \quad \begin{array}{c|c}
d \in \mathbb{N} \\
n \in \mathbb{N} \\
n \le d \\
\vdash \\
n < d \lor n > 0
\end{array}$$

$$\underline{DLF}$$

Our bridge controller being **deadlock-free** means that cars can **always** enter (via **ML_out**) or leave (via **ML_in**) the island-bridge compound.

Can we <u>formally</u> discharge this <u>PO</u> for our <u>initial model</u> m₀?

35 of 124

Example Inference Rules (4)





A goal is proved if it can be assumed.



Assuming *false* (\perp), anything can be proved.



true (⊤) is proved, regardless of the assumption.



An expression being equal to itself is proved, regardless of the assumption.

Example Inference Rules (5)



$$\frac{H(\mathbf{F}), \mathbf{E} = \mathbf{F} \vdash P(\mathbf{F})}{H(\mathbf{E}), \mathbf{E} = \mathbf{F} \vdash P(\mathbf{E})} \quad \mathbf{EQ_LR}$$

To prove a goal P(E) assuming H(E), where both P and H depend on expression E, it <u>suffices</u> to prove P(F) assuming H(F), where both P and H depend on expression F, given that E is equal to F.

$$\frac{H(\mathbf{E}), \mathbf{E} = \mathbf{F} \vdash P(\mathbf{E})}{H(\mathbf{F}), \mathbf{E} = \mathbf{F} \vdash P(\mathbf{F})} \quad \mathbf{EQ_RL}$$

To prove a goal P(F) assuming H(F), where both P and H depend on expression F, it suffices to prove P(E) assuming H(E), where both P and H depend on expression E, given that E is equal to F.

37 of 124

Discharging PO of DLF: Exercise

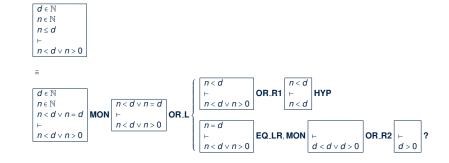


$$\begin{array}{c|c}
A(c) & & & & & \\
I(c, \mathbf{v}) & & & & & \\
 & G_1(c, \mathbf{v}) \vee \cdots \vee G_m(c, \mathbf{v})
\end{array}$$

$$\underline{DLF} \quad \begin{array}{c}
d \in \mathbb{N} \\
n \in \mathbb{N} \\
n \leq d \\
\vdash \\
n < d \vee n > 0
\end{array}$$

Discharging PO of DLF: First Attempt



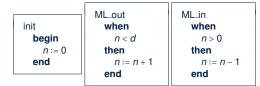


39 of 124

Why Did the DLF PO Fail to Discharge?



- In our first attempt, proof of the 2nd case failed: + d > 0
- This *unprovable* sequent gave us a good hint:
 - For the model under consideration (m₀) to be deadlock-free,
 it is required that d > 0. [≥ 1 car allowed in the IB compound]
 - But current **specification** of m_0 **not** strong enough to entail this:
 - $\neg(d > 0) \equiv d \le 0$ is possible for the current model
 - Given **axm0**₋**1** : *d* ∈ N
 - \Rightarrow d = 0 is allowed by m_0 which causes a **deadlock**.
- Recall the *init* event and the two *guarded* events:



When d = 0, the disjunction of guards evaluates to **false**: $0 < 0 \lor 0 > 0$ \Rightarrow As soon as the system is initialized, it **deadlocks immediately**

as no car can either enter or leave the IR compound!!

40 of 124



Fixing the Context of Initial Model

• Having understood the failed proof, we add a proper **axiom** to m_0 :

axioms: axm0_2: d > 0

• We have effectively elaborated on REQ2:

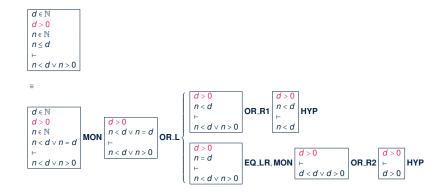
REQ2 The number of cars on bridge and island is limited but positive.

- Having changed the context, an <u>updated</u> sequent will be generated for the PO/VC rule of deadlock freedom.
- Is this new sequent now *provable*?

41 of 124

Discharging PO of DLF: Second Attempt



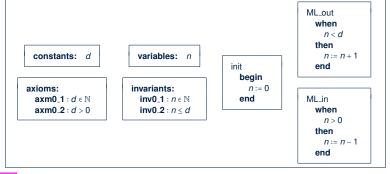


42 of 124

Initial Model: Summary



- The final version of our *initial model* m_0 is **provably correct** w.r.t.:
 - Establishment of *Invariants*
 - o Preservation of *Invariants*
 - Deadlock Freedom
- Here is the <u>final</u> **specification** of m_0 :

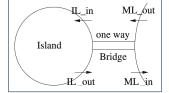


43 of 124

Model m_1 : "More Concrete" Abstraction



- First *refinement* has a more *concrete* perception of the bridge controller:
 - We "zoom in" by observing the system from closer to the ground, so that the island-bridge compound is split into:
 - the island
 - the (one-way) bridge



- Nonetheless, traffic lights and sensors remain abstracted away!
- That is, we focus on these two *requirement*:

F	REQ1	The system is controlling cars on a bridge connecting the mainland to an island.	
R	REQ3	The bridge is one-way or the other, not both at the same time.	

• We are **obliged to prove** this **added concreteness** is **consistent** with m_0 .

Model m_1 : Refined State Space

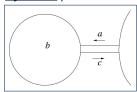


1. The **static** part is the same as m_0 's:

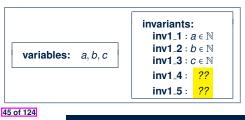
constants: d

axioms: $axm0_1: d \in \mathbb{N}$ $axm0_2: d > 0$

2. The dynamic part of the *concrete state* consists of three *variables*:



- a: number of cars on the bridge, heading to the island
- b: number of cars on the island
- c: number of cars on the bridge, heading to the mainland



- inv1_1, inv1_2, inv1_3 are typing constraints.
- √ inv1_4 links/glues the abstract and concrete states.
- √ inv1_5 specifies that the bridge is one-way.

Model m_1 : State Transitions via Events

- The system acts as an ABSTRACT STATE MACHINE (ASM): it evolves as actions of enabled events change values of variables, subject to invariants.
- We first consider the "old" **events** already existing in m_0 .
- Concrete/Refined version of event ML_out:



- Meaning of ML_out is refined: a car exits mainland (getting on the bridge).
- o ML_out enabled only when:
 - the bridge's current traffic flows to the island
 - number of cars on both the bridge and the island is limited
- Concrete/Refined version of event ML_in:



- Meaning of ML_in is refined:
 - a car enters mainland (getting off the bridge).
- o ML_in enabled only when:

there is some car on the bridge heading to the mainland.

46 of 124

Model m₁: Actions vs. Before-After Predicates on DE

Consider the concrete/refined version of actions of m₀'s two events:

Events



Before-after predicates c' = c - 1 $a' = a + 1 \land b' = b \land$ c' = c

- An event's actions are a specification: "c becomes c 1 after the transition".
- The **before-after predicate** (**BAP**) "c' = c 1" expresses that c' (the **post-state** value of c) is one less than c (the **pre-state** value of c).
- Given that the *concrete state* consists of three variables:
 - An event's actions only specify those changing from pre-state to post-state.

[e.q., c' = c - 1]

• Other unmentioned variables have their **post**-state values remain unchanged.

[e.g., $a' = a \wedge b' = b$]

 When we express proof obligations (POs) associated with events, we use BAP. 47 of 124

States & Invariants: Abstract vs. Concrete



- m_0 refines m_1 by introducing more *variables*:
 - Abstract State

(of m_0 being refined):

Concrete State (of the refinement model m_1): variables: n variables: a.b.c

Accordingly, invariants may involve different states:

Abstract Invariants (involving the *abstract* state only): invariants: $inv0_1: n \in \mathbb{N}$ **inv0_2** : *n* ≤ *d*

Concrete Invariants (involving at least the concrete state): invariants: inv1_1 : a ∈ N inv1_2 : b ∈ N inv1 $_3$: $_c \in \mathbb{N}$ $inv1_4: a+b+c=n$ $inv1_5: a = 0 \lor c = 0$

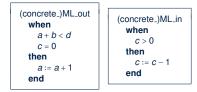


Events: Abstract vs. Concrete

- When an *event* exists in both models m_0 and m_1 , there are two versions of it:
 - The abstract version modifies the abstract state.



• The *concrete* version modifies the *concrete* state.

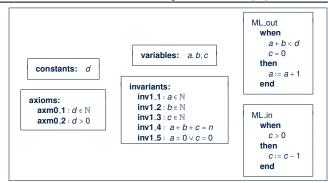


 A <u>new event</u> may <u>only</u> exist in m₁ (the <u>concrete</u> model): we will deal with this kind of events later, separately from "redefined/overridden" events.

49 of 124

PO of Refinement: Components (1)





- c: list of constants
- A(c): list of axioms
- v and v': abstract variables in pre- & post-states
- $v \cong \langle n \rangle, v' \cong \langle n \rangle$
- w and w': concrete variables in pre- & post-states $w \cong \langle a, b, c \rangle$, $w' \cong \langle a', b', c' \rangle$
- I(c, v): list of abstract invariants

⟨inv0_1, inv0_2⟩

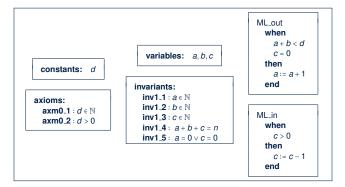
 $\langle axm0_{-1} \rangle$

- J(c, v, w): list of concrete invariants
- (inv1_1, inv1_2, inv1_3, inv1_4, inv1_5)

50 of 124

PO of Refinement: Components (2)





G(c, v): list of guards of the abstract event

$$G(\langle d \rangle, \langle n \rangle)$$
 of $ML_out \cong \langle n < d \rangle$, $G(c, v)$ of $ML_in \cong \langle n > 0 \rangle$

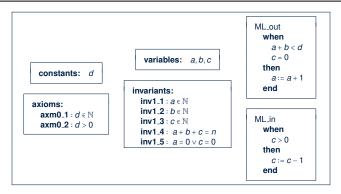
H(c, w): list of guards of the concrete event

$$H(\langle d \rangle, \langle a, b, c \rangle)$$
 of ML -out $\cong \langle a + b < d, c = 0 \rangle$, $H(c, w)$ of ML -in $\cong \langle c > 0 \rangle$

51 of 124

PO of Refinement: Components (3)





• E(c, v): effect of the **abstract event**'s actions i.t.o. what variable values **become**

$$E(\langle d \rangle, \langle n \rangle)$$
 of ML_out $\cong \langle n+1 \rangle$, $E(\langle d \rangle, \langle n \rangle)$ of ML_out $\cong \langle n-1 \rangle$

• F(c, w): effect of the **concrete event**'s actions i.t.o. what variable values **become**

$$F(c, v)$$
 of ML_out $\cong \langle a+1, b, c \rangle$, $F(c, w)$ of ML_out $\cong \langle a, b, c-1 \rangle$



Sketching PO of Refinement

The PO/VC rule for a *proper refinement* consists of two parts:

1. Guard Strengthening



- A concrete event is enabled if its abstract counterpart is enabled.
- A concrete transition <u>always</u> has an abstract counterpart.

2. Invariant Preservation



- A concrete event performs a transition on concrete states.
- This concrete state transition must be consistent with how its abstract counterpart performs a corresponding abstract transition.

Note. *Guard strengthening* and *invariant preservation* are only <u>applicable</u> to events that might be *enabled* after the system is launched.

The special, non-guarded init event will be discussed separately later.

Refinement Rule: Guard Strengthening



 Based on the components, we are able to formally state the PO/VC Rule of Guard Strengthening for Refinement:

```
 \begin{array}{c|c} A(c) \\ I(c, v) \\ J(c, v, w) \\ H(c, w) \\ \vdash \\ G_i(c, v) \end{array}  \quad \text{where } G_i \text{ denotes a } \underline{\text{single } \textit{guard}} \text{ condition}  of the \textit{abstract} event
```

How many sequents to be proved?

[# abstract guards]

o For ML_out, only one abstract guard, so one sequent is generated :

• Exercise. Write ML_in's PO of Guard Strengthening for Refinement.

54 of 124

PO Rule: Guard Strengthening of *ML_out*

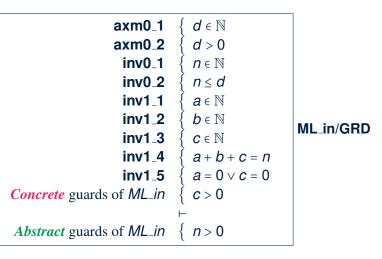


```
axm0<sub>1</sub>
                                           d \in \mathbb{N}
                         axm<sub>0</sub> 2
                                           d > 0
                           inv0<sub>1</sub>
                                           n \in \mathbb{N}
                           inv<sub>0_2</sub>
                                           n < d
                           inv1_1
                                           a \in \mathbb{N}
                           inv1<sub>2</sub>
                                           b \in \mathbb{N}
                           inv1 3
                                           c \in \mathbb{N}
                                                                   ML_out/GRD
                           inv1 4
                                           a+b+c=n
                           inv1 5
                                           a = 0 \lor c = 0
                                           a+b < d
Concrete guards of ML_out
                                           c = 0
Abstract guards of ML_out
```

55 of 124

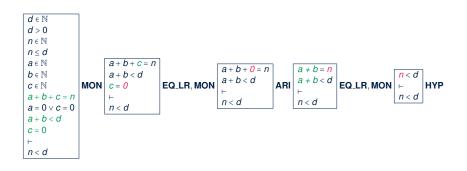
PO Rule: Guard Strengthening of ML_in





Proving Refinement: ML_out/GRD

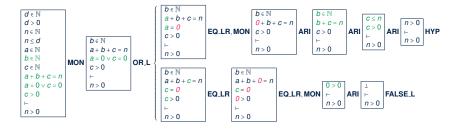




57 of 124

Proving Refinement: ML_in/GRD





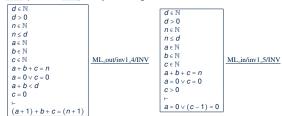
58 of 124

Refinement Rule: Invariant Preservation



 Based on the components, we are able to formally state the PO/VC Rule of Invariant Preservation for Refinement:

- How many **sequents** to be proved? [#concrete evts × #concrete invariants]
- Here are two (of the ten) sequents generated:



• Exercises. Specify and prove other eight POs of Invariant Preservation.

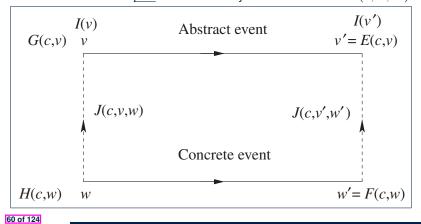
59 of 124

Visualizing Inv. Preservation in Refinement LASSONDE



Each **concrete** event (w to w') is **simulated by** an **abstract** event (v to v'):

- abstract & concrete pre-states related by concrete invariants J(c, v, w)
- abstract & concrete post-states related by concrete invariants J(c, v', w')



INV PO of m_1 : ML_out/inv1_4/INV



Proving Refinement: ML_out/inv1_4/INV



```
axm0_1
                                              d \in \mathbb{N}
                                axm0_2
                                              d > 0
                                 inv0_1
                                              n \in \mathbb{N}
                                 inv0_2
                                              n \le d
                                 inv1_1
                                              a \in \mathbb{N}
                                 inv1<sub>-2</sub>
                                             b \in \mathbb{N}
                                 inv1_3
                                 inv1_4
                                              a+b+c=n
                                 inv1_5
                                              a = 0 \lor c = 0
                                              a + b < d
           Concrete guards of ML_out
                                              c = 0
            Concrete invariant inv1_4
                                             (a+1)+b+c=(n+1)
with ML_out's effect in the post-state
```

ML_out/inv1_4/INV

61 of 124

INV PO of m₁: ML_in/inv1_5/INV

Concrete invariant **inv1**_5

with ML_in's effect in the post-state



axm0_1 $d \in \mathbb{N}$ axm0₂ d > 0inv0_1 $n \in \mathbb{N}$ inv0_2 $n \le d$ inv1_1 $a \in \mathbb{N}$ inv1₂ $b \in \mathbb{N}$ ML_in/inv1_5/INV inv1_3 $c \in \mathbb{N}$ inv1₄ a+b+c=ninv1₅ $a = 0 \lor c = 0$ Concrete guards of ML_in *c* > 0

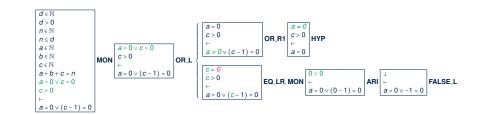
 $a = 0 \lor (c - 1) = 0$

d > 0 $n \in \mathbb{N}$ $n \leq d$ $a \in \mathbb{N}$ $b \in \mathbb{N}$ a+b+c=na+b+c=n $c \in \mathbb{N}$ MON ⊢ ARI EQ_LR. MON ⊢ EQ a+b+c=n(a + 1) + b + c = (n + 1)a + b + c + 1 = n + 1n+1 = n+1 $a = 0 \lor c = 0$ a + b < dc = 0(a+1)+b+c=(n+1)

63 of 124

Proving Refinement: ML_in/inv1_5/INV





62 of 124

LASSONDE SCHOOL OF ENGINEERING

Initializing the Refined System m_1

- Discharging the **twelve sequents** proved that:
 - o concrete invariants preserved by ML_out & ML_in
 - o concrete guards of ML_out & ML_in entail their abstract counterparts
- What's left is the specification of how the **ASM**'s *initial state* looks like:

init

begin

a := 0

b := 0

c := 0

end

- √ No cars on bridge (heading either way) and island
- ✓ Initialization always possible: guard is *true*.
- √ There is no pre-state for init.
 - : The RHS of := must not involve variables.
 - ∴ The <u>RHS</u> of := may <u>only</u> involve constants.
- √ There is only the post-state for init.
 - \therefore Before-After Predicate: $a' = 0 \land b' = 0 \land c' = 0$

65 of 124



PO of m₁ Concrete Invariant Establishment LASSONDE

- Some (new) formal components are needed:
 - K(c): effect of **abstract init**'s actions:

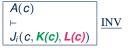
e.g.,
$$K(\langle d \rangle)$$
 of init $\widehat{=} \langle 0 \rangle$

- v' = K(c): **before-after predicate** formalizing **abstract** init's actions
 - e.g., BAP of *init*: $\langle n' \rangle = \langle 0 \rangle$
- *L(c)*: effect of *concrete init*'s actions:

e.g.,
$$K(\langle d \rangle)$$
 of init $\widehat{=} \langle 0, 0, 0 \rangle$

- w' = L(c): **before-after predicate** formalizing **concrete** init's actions
 - e.g., BAP of *init*: $\langle a', b', c' \rangle = \langle 0, 0, 0 \rangle$
- Accordingly, PO of *invariant establisment* is formulated as a sequent:







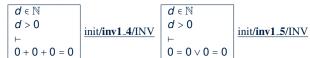
Discharging PO of m_1 Concrete Invariant Establishment



• How many *sequents* to be proved?

[# concrete invariants]

• Two (of the five) sequents generated for *concrete init* of m_1 :



• Can we discharge the **PO** init/inv1_4/INV ?



Can we discharge the PO init/inv1_5/INV ?



Model m_1 : New, Concrete Events



- The system acts as an ABSTRACT STATE MACHINE (ASM): it evolves as actions of enabled events change values of variables, subject to invariants.
- Considered *concrete/refined events* already existing in m_0 : $ML_out & ML_in$
- New event IL_in:

67 of 124



- o IL_in denotes a car entering the island (getting off the bridge).
- *IL_in enabled* only when:
 - The bridge's current traffic flows to the island.
 - **Q**. Limited number of cars on the bridge and the island?
 - A. Ensured when the earlier ML_out (of same car) occurred
- New event IL_out:

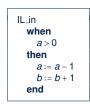


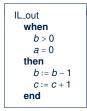
- *IL_out* denotes a car exiting the island (getting on the bridge).
- IL_out enabled only when:
 - There is some car on the island.
 - The bridge's current traffic flows to the mainland.



Model m_1 : BA Predicates of Multiple Actions ASSONDE

Consider **actions** of m_1 's two **new** events:





• What is the **BAP** of **ML_in**'s **actions**?

$$a' = a - 1 \land b' = b + 1 \land c' = c$$

• What is the **BAP** of **ML_in**'s **actions**?

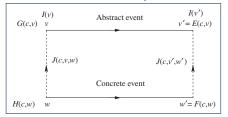
$$a' = a \wedge b' = b - 1 \wedge c' = c + 1$$

69 of 124



Visualizing Inv. Preservation in Refinement LASSONDE

Recall how a concrete event is simulated by its abstract counterpart:



- For each *new* event:
 - Strictly speaking, it does not have an abstract counterpart.
 - It is **simulated by** a special **abstract** event (transforming v to v'):

skip begin end

- skip is a "dummy" event: non-guarded and does nothing
- Q. BAP of the skip event?

A. n' = n

70 of 124

Refinement Rule: Invariant Preservation



- The new events *IL_in* and *IL_out* do not exist in **m**₀, but:
 - They exist in m₁ and may impact upon the concrete state space.
 - They *preserve* the *concrete invariants*, just as *ML_out* & *ML_in* do.
- Recall the PO/VC Rule of Invariant Preservation for Refinement:

 $\begin{array}{c} A(c) \\ I(c,v) \\ J(c,v,w) \\ H(c,w) \\ \vdash \\ J(c,E(c,v),F(c,w)) \end{array}$ where J_i denotes a single concrete invariant

- How many **sequents** to be proved? [# new evts × # concrete invariants]
- o Here are two (of the ten) sequents generated:

```
d > 0
                                                                d > 0
n \in \mathbb{N}
                                                                 n \in \mathbb{N}
n < d
                                                                 n \le d
                                                                 a \in \mathbb{N}
a \in \mathbb{N}
b \in \mathbb{N}
                                                                 b \in \mathbb{N}
                                     IL_in/inv1_4/INV
                                                                                             IL_in/inv1_5/INV
c \in \mathbb{N}
                                                                 c \in \mathbb{N}
a = 0 \lor c = 0
                                                                 a = 0 \lor c = 0
a > 0
                                                                a > 0
(a-1)+(b+1)+c=n
                                                                (a-1) = 0 \lor c = 0
```

• Exercises. Specify and prove other eight POs of Invariant Preservation.

71 of 124

INV PO of m_1 : IL_in/inv1_4/INV



```
axm0_1
                                           d \in \mathbb{N}
                             axm0<sub>2</sub>
                                           d > 0
                              inv0_1
                                           n \in \mathbb{N}
                              inv0_2
                                           n \le d
                              inv1_1
                                           a \in \mathbb{N}
                              inv1_2
                                           b \in \mathbb{N}
                                                                            IL_in/inv1_4/INV
                              inv1_3
                                           c \in \mathbb{N}
                              inv1_4
                                           a+b+c=n
                              inv1_5
                                           a = 0 \lor c = 0
                     Guards of IL_in
                                           a > 0
         Concrete invariant inv1_4
                                           (a-1)+(b+1)+c=n
with IL_in's effect in the post-state
```

INV PO of m_1 : IL_in/inv1_5/INV



```
axm0<sub>1</sub>
                                               d \in \mathbb{N}
                               axm0_2
                                               d > 0
                                inv0_1
                                               n \in \mathbb{N}
                                inv0_2
                                               n \leq d
                                inv1<sub>-</sub>1
                                               a \in \mathbb{N}
                                inv1_2
                                               b \in \mathbb{N}
                                                                           IL_in/inv1_5/INV
                                inv1_3
                                               c \in \mathbb{N}
                                inv1_4
                                               a+b+c=n
                                inv1_5
                                               a = 0 \lor c = 0
                      Guards of IL_in
                                              a > 0
         Concrete invariant inv1_5
                                              (a-1)=0\lor c=0
with IL_in's effect in the post-state
```

73 of 124

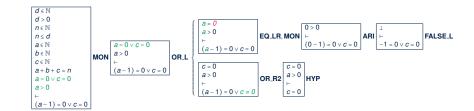
Proving Refinement: IL_in/inv1_4/INV



74 of 124

Proving Refinement: IL_in/inv1_5/INV



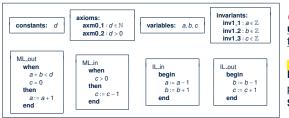


75 of 124

Livelock Caused by New Events Diverging



• An alternative m_1 (with inv1_4, inv1_5, and guards of new events removed):



Concrete invariants are **under-specified**: only typing constraints.

Exercises: Show that **Invariant Preservation** is provable, but **Guard Strengthening** is <u>not</u>.

- Say this alternative m₁ is implemented as is:
 IL_in and IL_out always enabled and may occur indefinitely, preventing other "old" events (ML_out and ML_in) from ever happening:
 - $\langle init, IL_in, IL_out, IL_in, IL_out, ... \rangle$
 - **Q**: What are the corresponding *abstract* transitions?
 - A: (init, skip, skip, skip, skip, . . .)

[≈ executing | while (true); |]

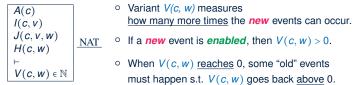
- We say that these two *new* events *diverge*, creating a *livelock*:
 - Different from a *deadlock*: always an event occurring (*IL_in* or *IL_out*).
 - But their indefinite occurrences contribute nothing useful.



PO of Convergence of New Events

The PO/VC rule for non-divergence/livelock freedom consists of two parts:

- Interleaving of new events charactered as an integer expression: variant.
- A variant V(c, w) may refer to constants and/or *concrete* variables.
- In the original m_1 , let's try **variants**: $2 \cdot a + b$
- 1. Variant Stays Non-Negative



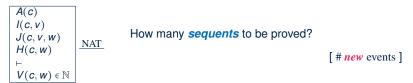
2. A New Event Occurrence Decreases Variant



PO of Convergence of New Events: NAT



• Recall: PO related to Variant Stays Non-Negative:



• For the **new** event **IL_in**:

$$d \in \mathbb{N} \qquad d > 0$$

$$n \in \mathbb{N} \qquad n \leq d$$

$$a \in \mathbb{N} \qquad b \in \mathbb{N} \qquad c \in \mathbb{N}$$

$$a + b + c = n \qquad a = 0 \lor c = 0$$

$$a > 0$$

$$\vdash$$

$$2 \cdot a + b \in \mathbb{N}$$

Exercises: Prove IL_in/NAT and Formulate/Prove IL_out/NAT.

78 of 124

PO of Convergence of New Events: VAR



• Recall: PO related to A New Event Occurrence Decreases Variant

• For the **new** event **IL_in**:

$$d \in \mathbb{N} \qquad d > 0$$

$$n \in \mathbb{N} \qquad n \le d$$

$$a \in \mathbb{N} \qquad b \in \mathbb{N} \qquad c \in \mathbb{N}$$

$$a + b + c = n \quad a = 0 \lor c = 0$$

$$a > 0$$

$$\vdash$$

$$2 \cdot (a - 1) + (b + 1) < 2 \cdot a + b$$

$$IL_{in}/VAR$$

Exercises: Prove IL_in/VAR and Formulate/Prove IL_out/VAR.

79 of 124

Convergence of New Events: Exercise



Given the original m_1 , what if the following *variant* expression is used:

variants : a + b

Are the formulated sequents still *provable*?

LASSONDE

PO of Refinement: Deadlock Freedom

- Recall:
 - We proved that the initial model m_0 is deadlock free (see **DLF**).
 - We proved, according to guard strengthening, that if a concrete event is enabled, then its abstract counterpart is enabled.
- PO of *relative deadlock freedom* for a *refinement* model:

Another way to think of the above PO:

The **refinement** does **not** introduce, in the **concrete**, any "new" **deadlock** scenarios **not** existing in the **abstract** state.

81 of 124

LASSONDE

PO Rule: Relative Deadlock Freedom m_1

```
axm0<sub>-</sub>1
                                        d \in \mathbb{N}
                         axm0<sub>2</sub>
                                        d > 0
                          inv0_1
                                        n \in \mathbb{N}
                          inv0_2
                                        n \le d
                          inv1<sub>-</sub>1
                                        a \in \mathbb{N}
                          inv1 2
                                        b \in \mathbb{N}
                          inv1_3
                                        c \in \mathbb{N}
                          inv1_4
                                        a+b+c=n
                                                                                                            DLF
                          inv1_5
                                        a = 0 \lor c = 0
                                                          quards of ML_out in mo
                                              n < d
Disjunction of abstract guards
                                              n > 0
                                                          guards of ML_{in} in m_0
                                              a+b < d \wedge c = 0
                                                                       guards of ML_out in m<sub>1</sub>
                                                                       guards of ML_in in m_1
                                                            c > 0
Disjunction of concrete guards
                                                                       guards of IL_in in m<sub>1</sub>
                                                            a > 0
                                                                       quards of IL_out in m1
                                                   b > 0 \land a = 0 }
```

82 of 124

Example Inference Rules (6)



 $\frac{H, \neg P \vdash Q}{H \vdash P \lor Q} \quad \mathsf{OR_R}$

To prove a disjunctive goal,

it suffices to prove one of the disjuncts, with the the <u>negation</u> of the the other disjunct serving as an additional <u>hypothesis</u>.

$$\frac{H,P,Q \vdash R}{H,P \land Q \vdash R} \quad \textbf{AND_L}$$

To prove a goal with a *conjunctive hypothesis*, it suffices to prove the same goal, with the two <u>conjuncts</u> serving as two separate hypotheses.

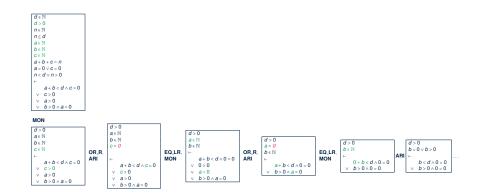
$$\frac{H \vdash P \qquad H \vdash Q}{H \vdash P \land Q} \quad \textbf{AND_R}$$

To prove a goal with a <u>conjunctive goal</u>, it suffices to prove each <u>conjunct</u> as a separate <u>goal</u>.

83 of 124

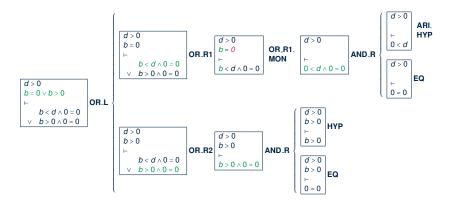
Proving Refinement: DLF of m_1







Proving Refinement: DLF of m_1 (continued) LASSONDE



85 of 124

86 of 124

First Refinement: Summary



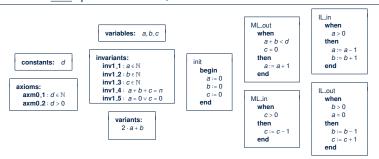
[init]

- The final version of our *first refinement* m_1 is *provably correct* w.r.t.:
 - Establishment of Concrete Invariants
- [old & new events]
- Preservation of Concrete Invariants

Strengthening of guards

- [old events
- o Convergence (a.k.a. livelock freedom, non-divergence)
- [new events]

- Relative *Deadlock* Freedom
- Here is the final specification of m_1 :

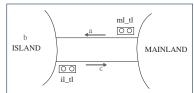


Model m_2 : "More Concrete" Abstraction



- 2nd *refinement* has even more *concrete* perception of the bridge controller:
 - We "zoom in" by observing the system from even closer to the ground, so that the one-way traffic of the bridge is controlled via:

ml_tl: a traffic light for exiting the ML il_tl: a traffic light for exiting the IL abstract variables a, b, c from m1 still used (instead of being replaced)



- Nonetheless, sensors remain abstracted away!
- That is, we focus on these three environment constraints:

ENV1	The system is equipped with two traffic lights with two colors: green and red.
ENV2	The traffic lights control the entrance to the bridge at both ends of it.
ENV3	Cars are not supposed to pass on a red traffic light, only on a green one.

• We are **obliged to prove** this **added concreteness** is **consistent** with m_1 . 87 of 124

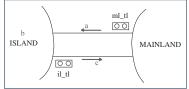
Model m_2 : Refined, Concrete State Space



1. The **static** part introduces the notion of traffic light colours:



2. The dynamic part shows the superposition refinement scheme:



- Abstract variables a, b, c from m₁ are still in use in m_2.
- Two new. concrete variables are introduced: ml_tl and il_tl
- Constrast: In m₁, abstract variable n is replaced by concrete variables a, b, c.



- ♦ inv2_1 & inv2_2: typing constraints
- ♦ inv2_3: being allowed to exit ML means cars within limit and no opposite traffic
- inv2_4: being allowed to exit IL means some car in IL and no opposite traffic



Model m_2 : Refining Old, Abstract Events

- The system acts as an ABSTRACT STATE MACHINE (ASM): it evolves as actions of enabled events change values of variables, subject to invariants.
- Concrete/Refined version of event ML_out:



- Recall the **abstract** guard of ML-out in m_1 : $(c = 0) \land (a + b < d)$
 - ⇒ Unrealistic as drivers should **not** know about a, b, c!
- *ML_out* is *refined*: a car exits the ML (to the bridge) only when:
 - the traffic light ml_tl allows
- Concrete/Refined version of event IL_out:



- Recall the **abstract** guard of $IL_{-}out$ in m_1 : $(a = 0) \land (b > 0)$
 - \Rightarrow Unrealistic as drivers should **not** know about a, b, c!
- o IL_out is refined: a car exits the IL (to the bridge) only when:
 - the traffic light *il_tl* allows
- Q1. How about the other two "old" events IL_in and ML_in?
- **A1**. No need to *refine* as already *quarded* by *ML_out* and *IL_out*.
- **Q2**. What if the driver disobeys *ml_tl* or *il_tl*?

[A2. ENV3]

89 of 124

Model m_2 : New, Concrete Events



- The system acts as an **ABSTRACT STATE MACHINE (ASM)**: it *evolves* as *actions of enabled events* change values of variables, subject to *invariants*.
- Considered *events* already existing in m_1 :
 - o ML_out & IL_out

[REFINED]

○ IL_in & ML_in

[UNCHANGED]

New event ML_tl_green:



- *ML_tl_green* denotes the traffic light *ml_tl* turning green.
- o ML_tl_green enabled only when:
- the traffic light not already green
 - limited number of cars on the bridge and the island
 - No opposite traffic

 $[\Rightarrow ML_out$'s **abstract** guard in m_1]

New event IL_tl_green:



90 of 124

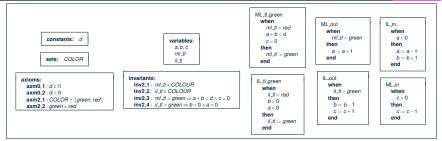
- ∘ *IL_tI_green* denotes the traffic light *iI_tI* turning green.
- o IL_tl_green enabled only when:
 - the traffic light not already green
 - some cars on the island (i.e., island not empty)
 - · No opposite traffic

[\Rightarrow *IL_out*'s **abstract** guard in m_1]

[⇒ IL out's



Invariant Preservation in Refinement m_2



Recall the PO/VC Rule of Invariant Preservation for Refinement:

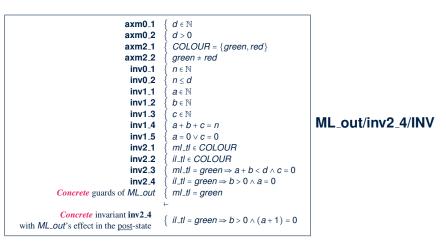
```
 \begin{array}{c|c} A(c) \\ I(c,v) \\ J(c,v,w) \\ H(c,w) \\ \vdots \\ J_i(c,E(c,v),F(c,w)) \end{array}  \quad \text{where } J_i \text{ denotes a } \underline{\text{single }} \text{ concrete invariant }
```

- How many **sequents** to be proved? [#concrete] evts $\times \#concrete$ invariants = 6×4
- We discuss two sequents: ML_out/inv2_4/INV and IL_out/inv2_3/INV

Exercises. Specify and prove (some of) other twenty-two POs of Invariant Preservation.

INV PO of m_2 : ML_out/inv2_4/INV





INV PO of m₂: IL_out/inv2_3/INV



```
axm0<sub>-</sub>1
                                             d \in \mathbb{N}
                                 axm0_2
                                              d > 0
                                axm2_1
                                               COLOUR = { green, red}
                                 axm2_2
                                              green ≠ red
                                 inv0_1
                                              n \in \mathbb{N}
                                 inv0_2
                                              n \le d
                                  inv1_1
                                              a \in \mathbb{N}
                                              b \in \mathbb{N}
                                 inv1_2
                                 inv1_3
                                              c \in \mathbb{N}
                                 inv1_4
                                              a+b+c=n
                                 inv1_5
                                             a = 0 \lor c = 0
                                              ml_tl ∈ COLOUR
                                  inv2_1
                                              iI_{-}tI \in COLOUR
                                 inv2_2
                                 inv2_3
                                               ml_tl = green \Rightarrow a + b < d \land c = 0
                                 inv2_4
                                              il_{-}tl = green \Rightarrow b > 0 \land a = 0
                                             il_tl = green
             Concrete guards of IL_out
            Concrete invariant inv2.3
                                             ml_{-}tl = green \Rightarrow a + (b-1) < d \land (c+1) = 0
with ML_out's effect in the post-state
```

IL_out/inv2_3/INV

93 of 124

Example Inference Rules (7)



$$\frac{H,P,Q \vdash R}{H,P,P \Rightarrow Q \vdash R} \quad \mathbf{IMP_L}$$

If a hypothesis *P* matches the <u>assumption</u> of another *implicative hypothesis P* ⇒ *Q*, then the <u>conclusion</u> *Q* of the *implicative hypothesis* can be used as a new hypothesis for the sequent.

$$\frac{H, P \vdash Q}{H \vdash P \Rightarrow Q} \quad \mathbf{IMP_R}$$

To prove an *implicative goal* $P \Rightarrow Q$, it suffices to prove its conclusion Q, with its assumption P serving as a new <u>hypotheses</u>.

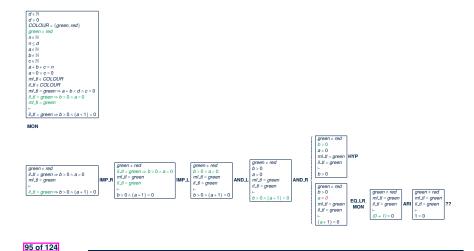
$$\frac{H, \neg Q \vdash P}{H, \neg P \vdash Q} \quad \mathsf{NOT_L}$$

To prove a goal Q with a *negative hypothesis* $\neg P$, it suffices to prove the <u>negated</u> hypothesis $\neg (\neg P) \equiv P$ with the <u>negated</u> original goal $\neg Q$ serving as a new hypothesis.

94 of 124

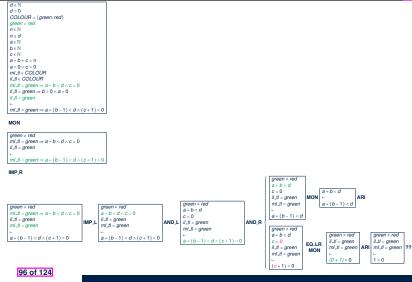
Proving ML_out/inv2_4/INV: First Attempt





Proving IL_out/inv2_3/INV: First Attempt







Failed: ML_out/inv2_4/INV, IL_out/inv2_3/INV

 Our first attempts of proving ML_out/inv2_4/INV and IL_out/inv2_3/INV both failed the 2nd case (resulted from applying IR AND_R):

$$green \neq red \land il_tl = green \land ml_tl = green \vdash 1 = 0$$

- This unprovable sequent gave us a good hint:
 - Goal 1 = 0 =false suggests that the safety requirements a = 0 (for inv2_4) and c = 0 (for inv2_3) contradict with the current m_2 .
 - Hyp. $il_{-}tl = green = ml_{-}tl$ suggests a **possible**, **dangerous state** of m_2 , where two cars heading <u>different</u> directions are on the <u>one-way</u> bridge:

(init	, ML_tl_green	, ML_out ,	<u>IL_in</u>	, IL_tI_green ,	IL_out	, ML_out)	
	d = 2		d=2	d = 2		4 0	d = 2	
		d = 2	u = 2	u = 2	d = 2	d = 2	u = 2	
	a' = 0	a'=0	a' = 1	a' = 0	a' = 0	a'=0	a' = 1	
	b'=0	b' = 0	b'=0	b' = 1	b' = 1	b' = 0	b' = 0	
	c'=0	c'=0	c'=0	c'=0	c'=0	c' = 1	c' = 1	
n	$nl_{-}tl' = red$	ml_tl' = green	$ml_{-}tl' = green$	ml_tl' = green	ml_tl' = green	$ml_{-}tl' = green$	$ml_{-}tl' = green$	
i	$I_{\perp}tI' = red$	$il\ tl' = red$	$iI_{-}tI' = red$	$iI_{-}tI' = red$	il tl' = green	il₋tl′ = green	il_tl' = green	





Fixing m_2 : Adding an Invariant

• Having understood the failed proofs, we add a proper *invariant* to m_2 :

```
invariants:
...
inv2_5 : ml_tl = red \leftarrow il_tl = red
```

• We have effectively resulted in an improved m_2 more faithful w.r.t. **REQ3**:

REQ3	3 The bi	ridge is one-way or t	the other, not both	at the same time.	
------	----------	-----------------------	---------------------	-------------------	--

- Having added this new invariant inv2_5:
 - Original 6 x 4 generated sequents to be <u>updated</u>: <u>inv2.5</u> a new hypothesis e.g., Are <u>ML_out/inv2_4/INV</u> and <u>IL_out/inv2_3/INV</u> now <u>provable</u>?
 - Additional 6 x 1 sequents to be generated due to this new invariant e.g., Are ML_tl_green/inv2_5/INV and IL_tl_green/inv2_5/INV provable?

98 of 124

INV PO of m_2 : ML_out/inv2_4/INV – Updated LASSONDE



```
axm0_1
                                               d \in \mathbb{N}
                                axm0_2
                                               d > 0
                                 axm2<sub>-</sub>1
                                               COLOUR = {green, red}
                                               green ≠ red
                                 axm2_2
                                  inv0_1
                                               n \in \mathbb{N}
                                  inv0_2
                                               n < d
                                  inv1_1
                                               a \in \mathbb{N}
                                  inv1_2
                                               b \in \mathbb{N}
                                  inv1_3
                                               c \in \mathbb{N}
                                               a+b+c=n
                                  inv1_4
                                                                                            ML out/inv2 4/INV
                                  inv1<sub>.5</sub>
                                               a = 0 \lor c = 0
                                  inv2_1
                                               ml_tl ∈ COLOUR
                                               il_tl ∈ COLOUR
                                  inv2_2
                                  inv2_3
                                               mI_{-}tI = green \Rightarrow a + b < d \land c = 0
                                  inv2_4
                                               iI_{t}I = green \Rightarrow b > 0 \land a = 0
                                  inv2<sub>-</sub>5
                                               ml_{-}tl = red \lor il_{-}tl = red
           Concrete guards of ML_out
                                               ml_tl = green
            Concrete invariant inv2_4
                                               iI_{t}I = green \Rightarrow b > 0 \land (a+1) = 0
with ML_out's effect in the post-state
```

99 of 124

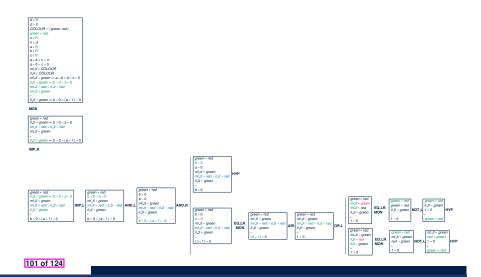
INV PO of m_2 : IL_out/inv2_3/INV – Updated



```
axm0<sub>-</sub>1
                                            \{d \in \mathbb{N}
                                axm0_2
                                              d > 0
                                              COLOUR = { green, red}
                                axm2_1
                                axm2_2
                                              green ≠ red
                                 inv0 1
                                              n \in \mathbb{N}
                                 inv<sub>0.2</sub>
                                              n \le d
                                             a \in \mathbb{N}
                                 inv1_1
                                 inv1_2
                                              b \in \mathbb{N}
                                 inv1_3
                                              c \in \mathbb{N}
                                              a+b+c=n
                                 inv1 4
                                                                                                       IL_out/inv2_3/INV
                                 inv1_5
                                              a = 0 \lor c = 0
                                 inv2_1
                                              ml_tl ∈ COLOUR
                                 inv2 2
                                              il_tl ∈ COLOUR
                                              ml_tl = green \Rightarrow a + b < d \land c = 0
                                 inv2_3
                                 inv2_4
                                              iI_{-}tI = areen \Rightarrow b > 0 \land a = 0
                                 inv2_5
                                              ml_tl = red \lor il_tl = red
            Concrete guards of IL_out
                                             \{ ml\_tl = green \Rightarrow a + (b-1) < d \land (c+1) = 0 
with ML_out's effect in the post-state
```



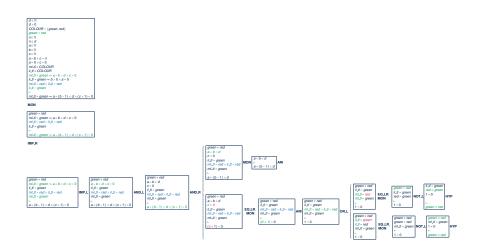
Proving ML_out/inv2_4/INV: Second Attempt LASSONDE



Proving IL_out/inv2_3/INV: Second Attempt

102 of 124





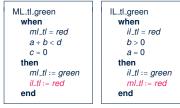
Fixing m_2 : Adding Actions



• Recall that an *invariant* was added to m_2 :

```
invariants:
inv2_5 : ml_tl = red \( \tilde{l}_tl = red
```

- Additional 6 × 1 sequents to be generated due to this new invariant:
 - e.g., ML_tl_green/inv2_5/INV [for ML_tl_green to preserve inv2_5]
 e.g., IL_tl_green/inv2_5/INV [for IL_tl_green to preserve inv2_5]
- For the above *sequents* to be *provable*, we need to revise the two events:



Exercise: Specify and prove ML_tl_green/inv2_5/INV & IL_tl_green/inv2_5/INV.

103 of 124

INV PO of m2: ML_out/inv2_3/INV



```
axm0<sub>-</sub>1
                                              d \in \mathbb{N}
                                axm0<sub>2</sub>
                                axm2_1
                                              COLOUR = {green, red}
                                axm2_2
                                              green ≠ red
                                 inv0_1
                                              n \in \mathbb{N}
                                 inv0_2
                                              n \le d
                                 inv1_1
                                              a \in \mathbb{N}
                                 inv1_2
                                              b \in \mathbb{N}
                                 inv1_3
                                              c \in \mathbb{N}
                                 inv1_4
                                              a+b+c=n
                                                                                                ML_out/inv2_3/INV
                                 inv1_5
                                              a = 0 \lor c = 0
                                              ml\_tl \in COLOUR
                                 inv2_1
                                 inv2_2
                                              il_tl ∈ COLOUR
                                 inv2_3
                                              mI_{-}tI = green \Rightarrow a + b < d \land c = 0
                                 inv2_4
                                              iI_{-}tI = green \Rightarrow b > 0 \land a = 0
                                 inv2_5
                                              ml_{-}tl = red \lor il_{-}tl = red
           Concrete guards of ML_out
                                              ml_tl = green
            Concrete invariant inv2_3
                                              mI_{-}tI = green \Rightarrow (a+1) + b < d \land c = 0
with ML_out's effect in the post-state
```



Proving ML_out/inv2_3/INV: First Attempt

```
COLOUR = {green, red}
green + red
a+b+c=na=0 \lor c=0
ml_tl ∈ COLOUR
il_tl ∈ COLOUR
if tl = areen \Rightarrow b > 0 \land a = 0
 ml_tl = areen
ml_{\perp}tl = areen \Rightarrow (a+1) + b < d \land c = 0
```



105 of 124

Failed: ML out/inv2 3/INV



• Our first attempt of proving ML_out/inv2_3/INV failed the 1st case (resulted from applying IR **AND_R**):

$$a+b < d \land c = 0 \land ml_tl = green \vdash (a+1) + b < d$$

• This *unprovable* sequent gave us a good hint:

• Goal (a+1) + b < d specifies the *capacity requirement*.

• Hypothesis $c = 0 \land ml_t = green$ assumes that it's safe to exit the ML.

• Hypothesis |a+b| < d is **not** strong enough to entail (a+1) + b < d.

e.g., d = 3, b = 0, a = 0[(a+1)+b < d evaluates to **true**]e.g., d = 3, b = 1, a = 0[(a+1)+b < d evaluates to **true** e.g., d = 3, b = 0, a = 1[(a+1)+b < d evaluates to **true**]e.g., d = 3, b = 0, a = 2[(a+1)+b < d evaluates to **false** e.g., d = 3, b = 1, a = 1[(a+1)+b < d evaluates to **false**]e.g., d = 3, b = 2, a = 0[(a+1)+b < d evaluates to **false**]

• Therefore, a + b < d (allowing one more car to exit ML) should be split:

 $a+b+1\neq d$ [more later cars may exit ML, *ml_tl* remains *green*] a + b + 1 = d

106 of 124

[no more later cars may exit ML, ml_tl turns red]

Fixing m_2 : Splitting ML_out and IL_out



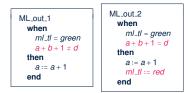
• Recall that *ML_out/inv2_3/INV* failed : two cases not handled separately:

$$a+b+1\neq d$$
 [more later cars may exit ML, ml_tl remains $green$]
 $a+b+1=d$ [no more later cars may exit ML, ml_tl turns red]

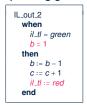
• Similarly, IL_out/inv2_4/INV would fail : two cases not handled separately:

$$b-1 \neq 0$$
 [more later cars may exit IL, il_tl remains $green$]
 $b-1=0$ [no more later cars may exit IL, il_tl turns red]

Accordingly, we split ML_out and IL_out into two with corresponding guards.







Exercise: Specify and prove ML_out/inv2_3/INV & IL_out/inv2_4/INV.

Exercise: Given the latest m_2 , how many sequents to prove for *invariant preservation*?

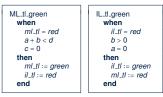
Exercise: Each split event (e.g., ML_out_1) refines its abstract counterpart (e.g., ML_out)?

107 of 124

m₂ Livelocks: New Events Diverging



- Recall that a system may *livelock* if the new events diverge.
- Current m₂'s two new events ML_tl_green and IL_tl_green may diverge:



 ML_tl_green and IL_tl_green both enabled and may occur indefinitely, preventing other "old" events (e.g., ML_out) from ever happening:

(init	,	ML_tl_green	, ML_out_1	, <u>IL_in</u>	, <u>IL_tI_green</u> ,	ML_tl_green ,	IL_tl_green ,)
	d = 2		d = 2	d = 2	d = 2	d = 2	d = 2	d = 2
	a' = 0		a' = 0	a' = 1	a' = 0	a' = 0	a' = 0	a' = 0
	b' = 0		b' = 0	b' = 0	b' = 1	b' = 1	b' = 1	b' = 1
	c'=0		c'=0	c'=0	c'=0	c'=0	c'=0	c'=0
	ml_tl = <mark>red</mark>		ml_tl' = green	$ml_{-}tl' = green$	ml_tl' = green	$ml_{-}tl' = red$	$ml_{\perp}tl' = green$	$ml_{-}tl' = red$
	il_tl = red		il tl' = red	il_tl' = red	il_tl' = red	il tl' = areen	$il\ tl' = red$	il tl' = areen

- ⇒ Two traffic lights keep changing colors so rapidly that **no** drivers can ever pass!
- Solution: Allow color changes between traffic lights in a disciplined way.



Fixing m_2 : Regulating Traffic Light Changes LASSONDE

We introduce two variables/flags for regulating traffic light changes:

- ml_pass is 1 if, since ml_tl was last turned green, at least one car exited the ML onto the bridge. Otherwise, ml_pass is 0.
- iI_pass is 1 if, since iI_tI was last turned green, at least one car exited the IL onto the bridge. Otherwise, iI_pass is 0.

```
IL_out_1
                                                                                                             ml_tl = red
                                                 ML_out_1
                                                                               when
                                                                                                            a+b < d
                                                   when
                                                                                 il_tl = areen
                                                                                                            c = 0
                                                     ml_tl = green
                                                                                 b ≠ 1
                                                                                                             il nass = 1
                                                     a+b+1\neq d
                                                                               then
                                                                                                           then
                                                                                 b := b - 1
                                                   then
                                                                                                             ml_tl := green
                                                    a:= a+1
                                                                                 c := c + 1
                                                                                                             il_tl := red
    variables: ml_pass.il_pass
                                                     ml pass := 1
                                                                                 il pass := 1
                                                                                                             ml_pass := 0
                                                   end
                                                                               end
                                                                                                           end
invariants:
  inv2_6 : ml_pass ∈ {0, 1}
                                                ML_out_2
                                                                             IL_out_2
                                                                                                         IL_tl_green
  inv2_7 : il_pass ∈ {0, 1}
                                                   when
                                                                               when
  inv2_8 : ml_tl = red \Rightarrow ml_pass = 1
                                                     ml_tl = areen
                                                                                 il_tl = areen
                                                                                                             iI_{-}tI = red
  inv2_9 : il\_tl = red \Rightarrow il\_pass = 1
                                                     a + b + 1 = d
                                                                                 b = 1
                                                                                                             b > 0
                                                   then
                                                                               then
                                                                                                             a = 0
                                                     a := a + 1
                                                                                 b := b - 1
                                                                                                             ml_pass = 1
                                                     ml \ tl := red
                                                                                 c := c + 1
                                                                                                           then
                                                     ml_pass := 1
                                                                                 iI_{-}tI := red
                                                                                                             il tl := areen
                                                   end
                                                                                 il_pass := 1
                                                                                                             ml \ tl := red
                                                                               end
                                                                                                             il_pass := 0
```

Fixing m_2 : Measuring Traffic Light Changes LASSONDE



111 of 124

end

Recall

109 of 124

- Interleaving of **new** events charactered as an integer expression: **variant**.
- \circ A variant V(c, w) may refer to constants and/or *concrete* variables.
- In the latest m_2 , let's try variants : $ml_pass + il_pass$
- Accordingly, for the **new** event **ML_tl_green**:

```
COLOUR = {green, red}
                                             green ≠ red
n \in \mathbb{N}
                                             n \leq d
a \in \mathbb{N}
                                             b \in \mathbb{N}
                                                                                    c \in \mathbb{N}
a+b+c=n
                                             a = 0 \lor c = 0
ml_tl ∈ COLOUR
                                             il_tl ∈ COLOUR
ml_{-}tl = green \Rightarrow a + b < d \land c = 0 il_{-}tl = green \Rightarrow b > 0 \land a = 0
                                                                                               ML_tl_green/VAR
ml_tl = red \lor il_tl = red
ml_pass ∈ {0, 1}
                                             il_pass ∈ {0, 1}
                                             iI_{-}tI = red \Rightarrow iI_{-}pass = 1
ml_{-}tl = red \Rightarrow ml_{-}pass = 1
ml_{-}tl = red
                                             a + b < d
                                                                                    c = 0
il_pass = 1
0 + il_pass < ml_pass + il_pass
```

<u>Exercises</u>: Prove ML_tl_green/VAR and Formulate/Prove IL_tl_green/VAR.

110 of 124

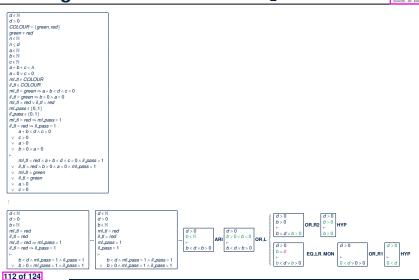
PO Rule: Relative Deadlock Freedom of m₂ LASSONDE



```
axm0_1
                       axm0_2
                                    d > 0
                                    COLOUR = {green, red}
                       aym2 1
                      axm2_2
                                    green + red
                        inv0 1
                                    n \in \mathbb{N}
                        inv0_2
                                    n < d
                                    a \in \mathbb{N}
                        inv1_1
                                    b \in \mathbb{N}
                        inv1_2
                        inv1_3
                                    c \in \mathbb{N}
                        inv1_4
                                    a+b+c=n
                        inv1_5
                                    a = 0 \lor c = 0
                                    ml_tl ∈ COLOUR
                        inv2_1
                        inv2_2
                                    iI\_tI \in COLOUR
                                    ml_tl = green \Rightarrow a + b < d \land c = 0
                        inv2_3
                        inv2 4
                                    iI_{-}tI = green \Rightarrow b > 0 \land a = 0
                        inv2 5
                                    ml tl = red \lor il tl = red
                                                                                                                                    DLF
                                    ml_pass ∈ {0, 1}
                        inv2_6
                        inv2_7
                                    il_pass ∈ {0, 1}
                        inv2_8
                                    ml\_tl = red \Rightarrow ml\_pass = 1
                        inv2_9
                                     iI_{-}tI = red \Rightarrow iI_{-}pass = 1
                                         a+b < d \land c = 0
                                                                guards of ML_out in m1
                                                                guards of ML_in in m1
Disjunction of abstract guards
                                                               guards of IL_in in m1
                                              b > 0 \land a = 0 guards of IL_out in m
                                          ml_t l = red \land a + b < d \land c = 0 \land il_pass = 1
                                                                                            guards of ML_tl_green in m2
                                              if tl = red \land h > 0 \land a = 0 \land ml page = 1
                                                                                            guards of IL_tl_green in m2
                                                         ml_{-}tl = areen \land a + b + 1 \neq d
                                                                                            quards of ML_out_1 in mo
                                                         ml_{-}tl = green \land a + b + 1 = d
                                                                                            quards of ML_out_2 in mo
Disjunction of concrete guards
                                                                   iI_{\perp}tI = areen \land b \neq 1
                                                                                            guards of /L_out_1 in mo
                                                                   iI_{-}tI = areen \land b = 1
                                                                                            guards of /L_out_2 in ma
                                                                                  a > 0
                                                                                            guards of ML_in in m2
                                                                                            guards of IL_in in m2
```

Proving Refinement: DLF of *m*₂





Second Refinement: Summary



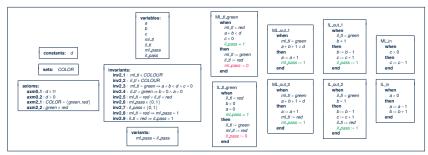
[init]

[old & new events]

[old events]

- The final version of our **second refinement** m_2 is **provably correct** w.r.t.:
 - Establishment of Concrete Invariants
 - Preservation of Concrete Invariants
 - Strengthening of guards
 - o *Convergence* (a.k.a. livelock freedom, non-divergence)
- [new events]

- Relative *Deadlock* Freedom
- Here is the final specification of m_2 :



113 of 124

Index (1)



Learning Outcomes

Recall: Correct by Construction

State Space of a Model

Roadmap of this Module

Requirements Document: Mainland, Island

Requirements Document: E-Descriptions

Requirements Document: R-Descriptions

Requirements Document:

Visual Summary of Equipment Pieces

Refinement Strategy

Model m_0 : Abstraction

114 of 124

Index (2)



Model m_0 : State Space

Model m_0 : State Transitions via Events

Model m_0 : Actions vs. Before-After Predicates

Design of Events: Invariant Preservation

Sequents: Syntax and Semantics

PO of Invariant Preservation: Sketch

PO of Invariant Preservation: Components

Rule of Invariant Preservation: Sequents

Inference Rules: Syntax and Semantics

Proof of Sequent: Steps and Structure

Example Inference Rules (1)

115 of 124

Index (3)



Example Inference Rules (2)

Example Inference Rules (3)

Revisiting Design of Events: ML_out

Revisiting Design of Events: ML_in

Fixing the Design of Events

Revisiting Fixed Design of Events: ML_out

Revisiting Fixed Design of Events: ML_in

Initializing the Abstract System m_0

PO of Invariant Establishment

Discharging PO of Invariant Establishment

System Property: Deadlock Freedom

Index (4)



PO of Deadlock Freedom (1)

PO of Deadlock Freedom (2)

Example Inference Rules (4)

Example Inference Rules (5)

Discharging PO of DLF: Exercise

Discharging PO of DLF: First Attempt

Why Did the DLF PO Fail to Discharge?

Fixing the Context of Initial Model

Discharging PO of DLF: Second Attempt

Initial Model: Summary

Model m_1 : "More Concrete" Abstraction

117 of 124

LASSONDE SCHOOL OF ENGINEERING

Index (5)

Model m₁: Refined State Space

Model m_1 : State Transitions via Events

Model m_1 : Actions vs. Before-After Predicates

States & Invariants: Abstract vs. Concrete

Events: Abstract vs. Concrete

PO of Refinement: Components (1)

PO of Refinement: Components (2)

PO of Refinement: Components (3)

Sketching PO of Refinement

Refinement Rule: Guard Strengthening
PO Rule: Guard Strengthening of ML_out

118 of 124

Index (6)



PO Rule: Guard Strengthening of ML_in

Proving Refinement: ML_out/GRD

Proving Refinement: ML_in/GRD

Refinement Rule: Invariant Preservation

Visualizing Inv. Preservation in Refinement

INV PO of m_1 : ML_out/inv1_4/INV

INV PO of m_1 : ML_in/inv1_5/INV

Proving Refinement: ML_out/inv1_4/INV

Proving Refinement: ML_in/inv1_5/INV

Initializing the Refined System m_1

PO of *m*₁ **Concrete Invariant Establishment**

119 of 124

Index (7)



Discharging PO of m_1

Concrete Invariant Establishment

Model m_1 : New, Concrete Events

Model m_1 : BA Predicates of Multiple Actions

Visualizing Inv. Preservation in Refinement

Refinement Rule: Invariant Preservation

INV PO of m_1 : IL_in/inv1_4/INV

INV PO of m₁: IL_in/inv1_5/INV

Proving Refinement: IL_in/inv1_4/INV

Proving Refinement: IL_in/inv1_5/INV

Livelock Caused by New Events Diverging

Index (8)



PO of Convergence of New Events

PO of Convergence of New Events: NAT

PO of Convergence of New Events: VAR

Convergence of New Events: Exercise

PO of Refinement: Deadlock Freedom

PO Rule: Relative Deadlock Freedom of m_1

Example Inference Rules (6)

Proving Refinement: DLF of m_1

Proving Refinement: DLF of m_1 (continued)

First Refinement: Summary

Model m_2 : "More Concrete" Abstraction

121 of 124

Index (9)



Model m_2 : Refined, Concrete State Space

Model m_2 : Refining Old, Abstract Events

Model m_2 : New, Concrete Events

Invariant Preservation in Refinement m_2

INV PO of m_2 : ML_out/inv2_4/INV

INV PO of m₂: IL_out/inv2_3/INV

Example Inference Rules (7)

Proving ML_out/inv2_4/INV: First Attempt

Proving IL_out/inv2_3/INV: First Attempt

Failed: ML_out/inv2_4/INV, IL_out/inv2_3/INV

Fixing m_2 : Adding an Invariant

122 of 124

Index (10)



INV PO of m₂: ML_out/inv2_4/INV – Updated

INV PO of m_2 : IL_out/inv2_3/INV – Updated

Proving ML_out/inv2_4/INV: Second Attempt

Proving IL_out/inv2_3/INV: Second Attempt

Fixing m_2 : Adding Actions

INV PO of m₂: ML_out/inv2_3/INV

Proving ML_out/inv2_3/INV: First Attempt

Failed: ML_out/inv2_3/INV

Fixing m_2 : Splitting ML_{out} and IL_{out}

m₂ Livelocks: New Events Diverging

Fixing m_2 : Regulating Traffic Light Changes

123 of 124

Index (11)



Fixing m_2 : Measuring Traffic Light Changes

PO Rule: Relative Deadlock Freedom of m_2

Proving Refinement: DLF of m_2 Second Refinement: Summary