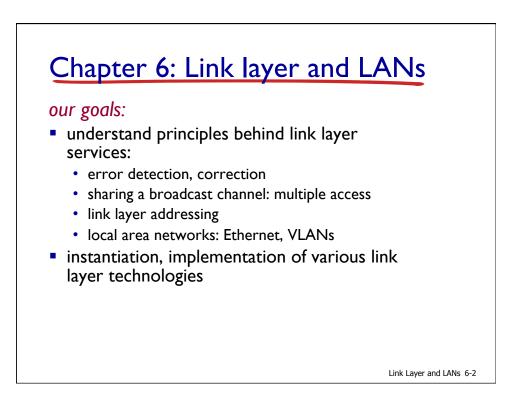
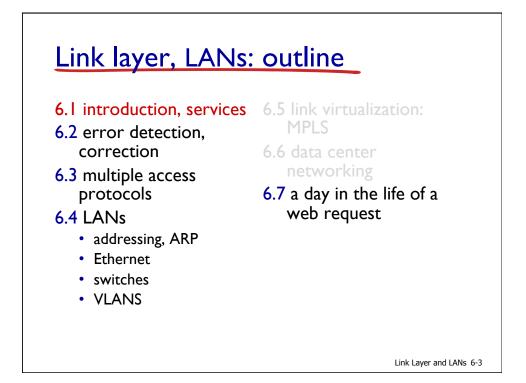
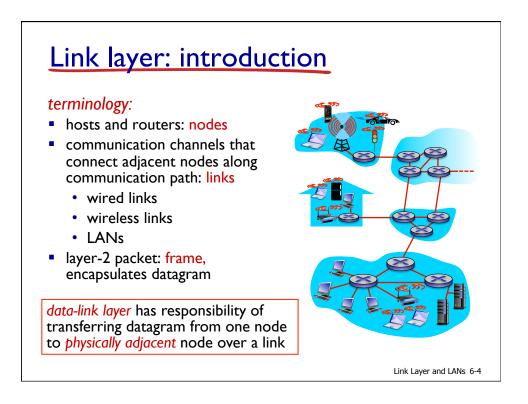
The Link Layer and LANs		
EECS3214		
2018-03-14	© All material copyright 1996-2016 J.F Kurose and K.W. Ross, All Rights Reserved	4-1

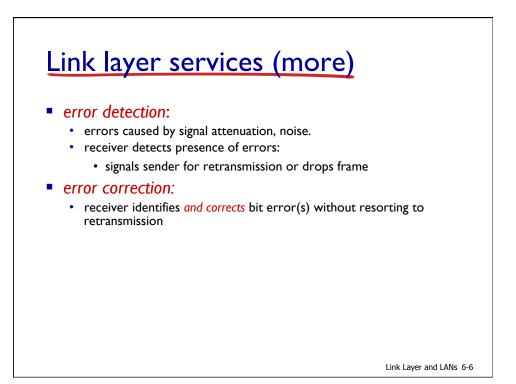


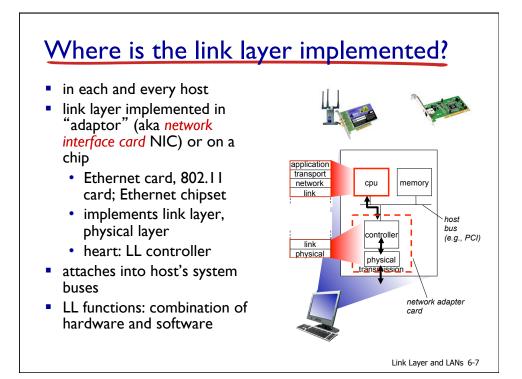


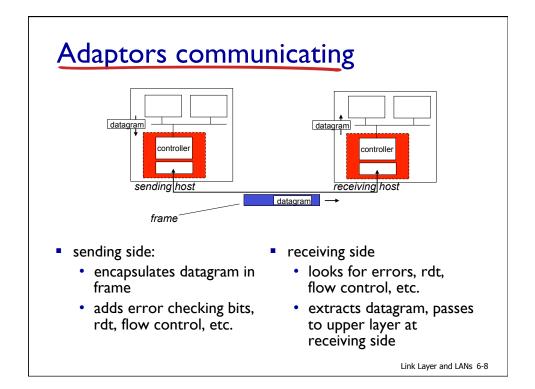


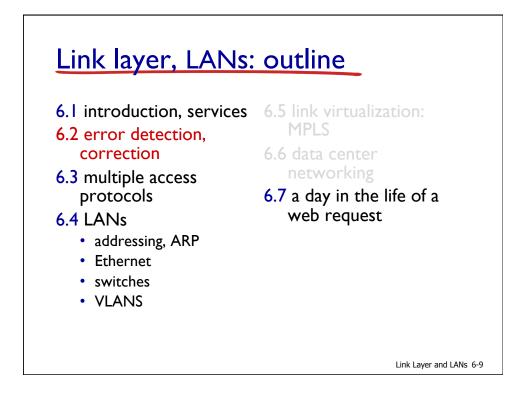
# Link layer services

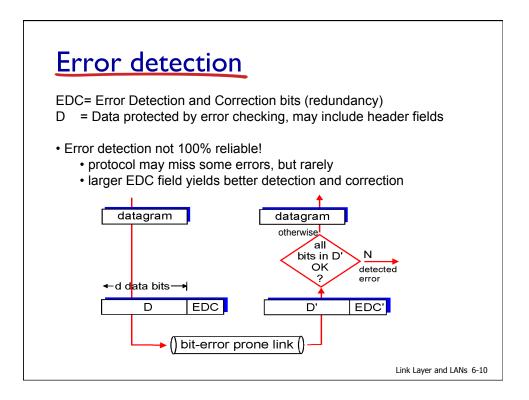
- framing
  - encapsulate datagram into frame, adding header, trailer
- link access
  - channel access if shared medium
  - MAC addresses used in frame headers to identify source, destination at the link layer
- reliable delivery between adjacent nodes
  - principles of reliable data transfer (section 3.4)
  - seldom used on low bit-error link (fiber, some twisted pair)
  - wireless links: high error rates
    - Q: why both link-level and end-end reliability?

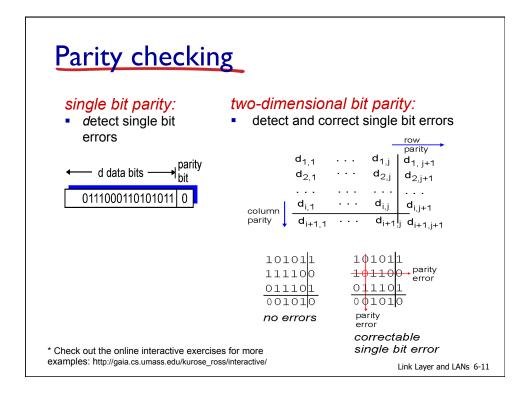


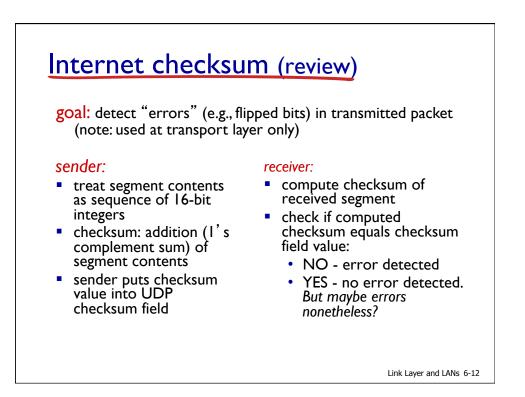


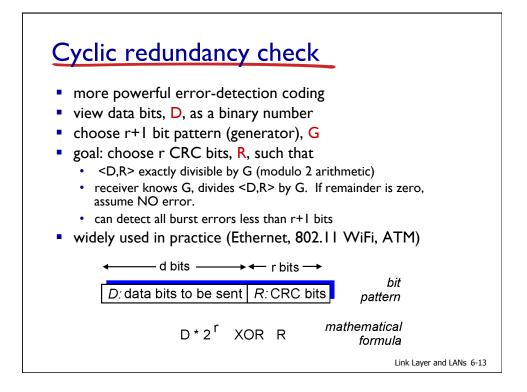


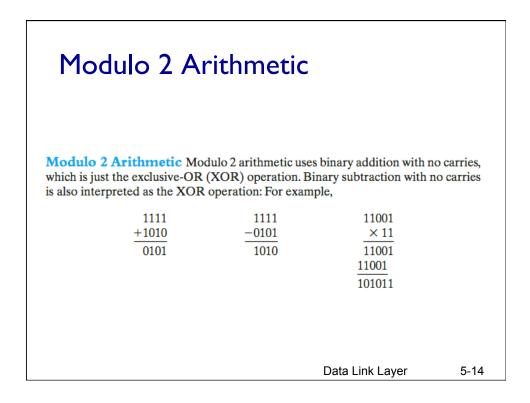


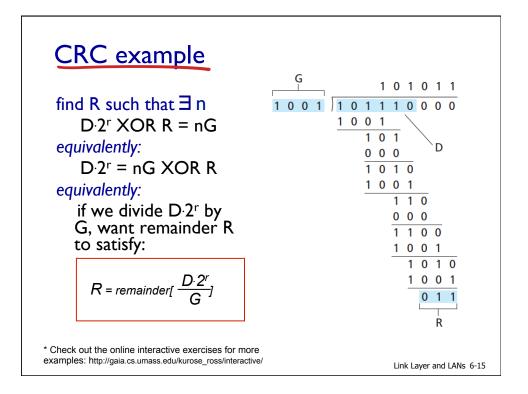


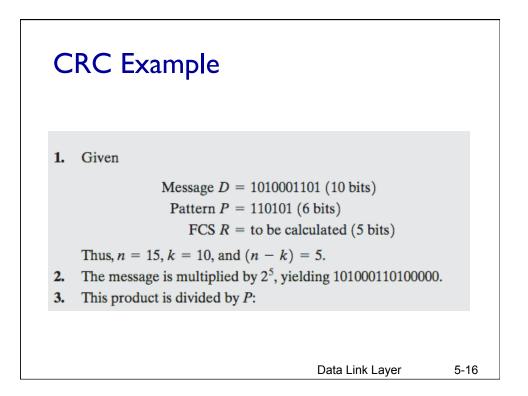


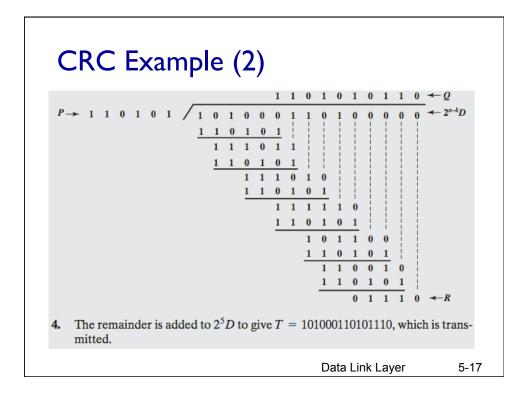


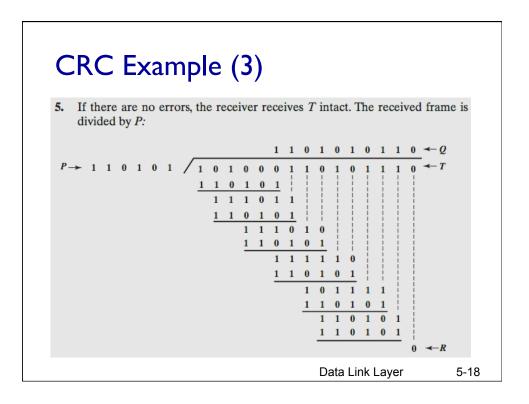


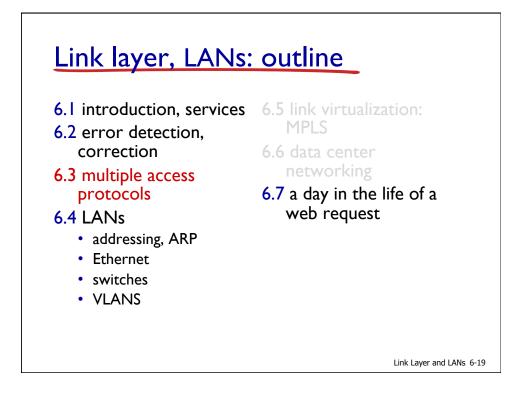


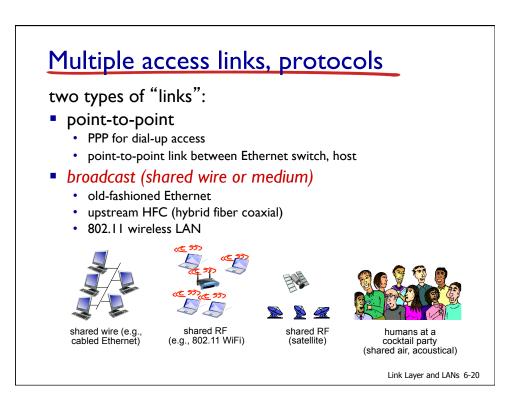










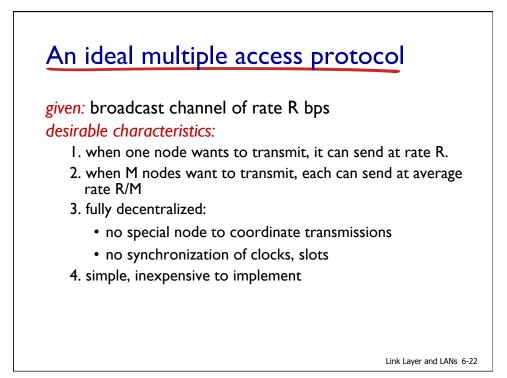


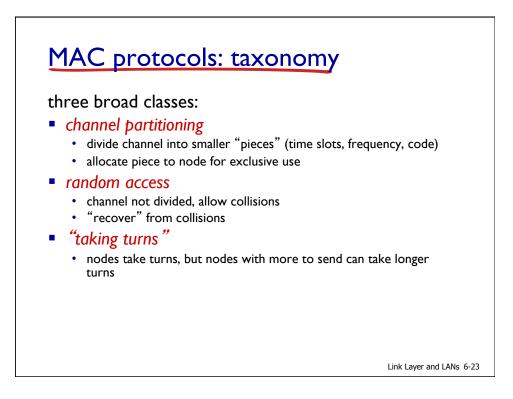


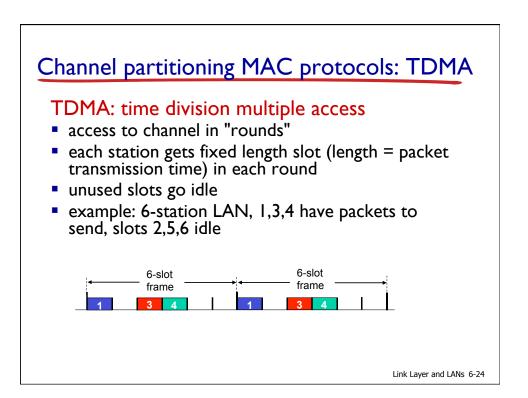
- single shared broadcast channel
- two or more simultaneous transmissions by nodes: interference
  - *collision* if node receives two or more signals at the same time

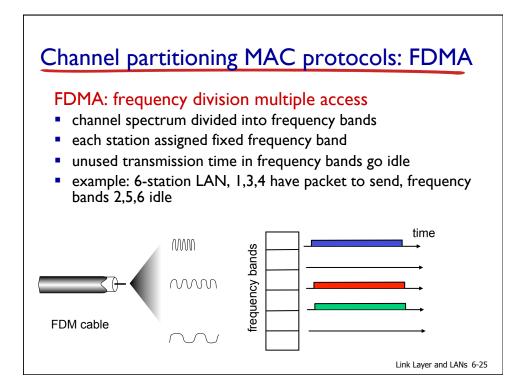
### multiple access protocol

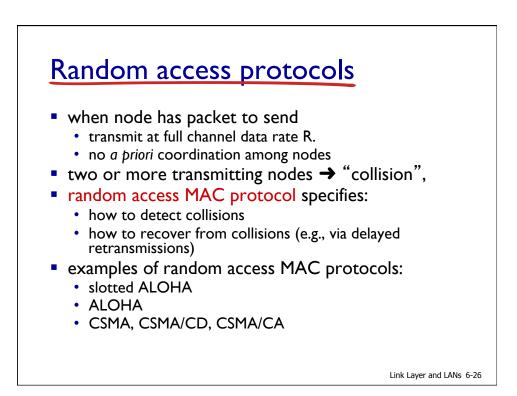
- distributed algorithm that determines how nodes share channel, i.e., determine when node can transmit
- communication about channel sharing must use channel itself!
  - no out-of-band channel for coordination











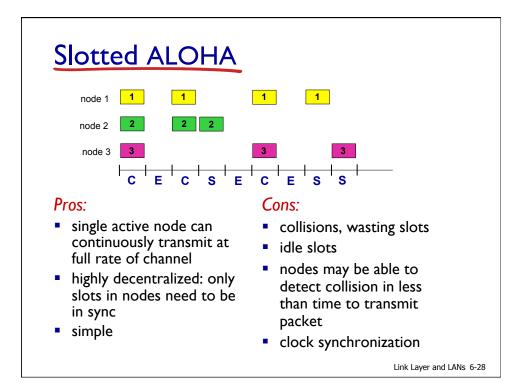


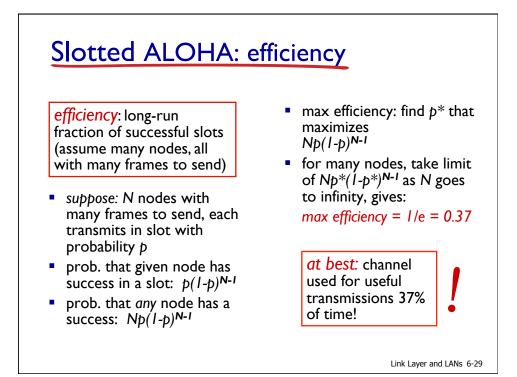
#### assumptions:

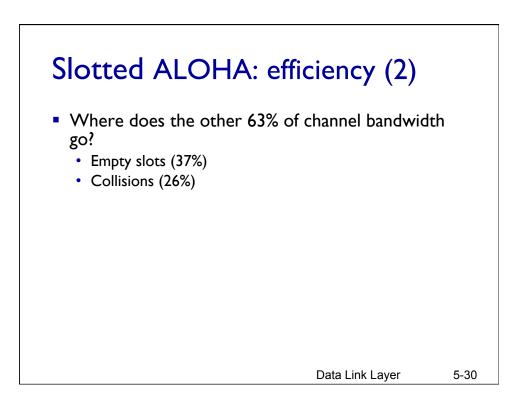
- all frames same size L bits
- time divided into equal size slots L/R (time to transmit one frame)
- nodes start to transmit only slot beginning
- nodes are synchronized
- if 2 or more nodes transmit in the same slot, all nodes detect collision
  - bus/hub LANs: measure signal strengths (voltage)

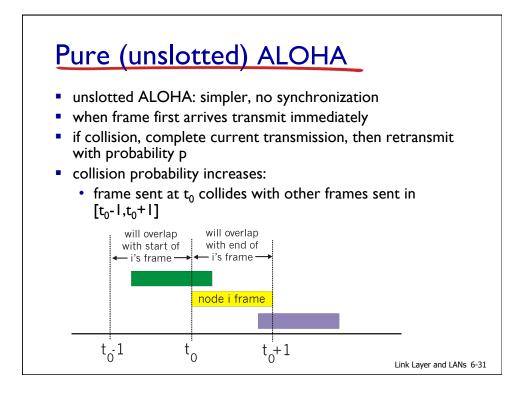
#### operation:

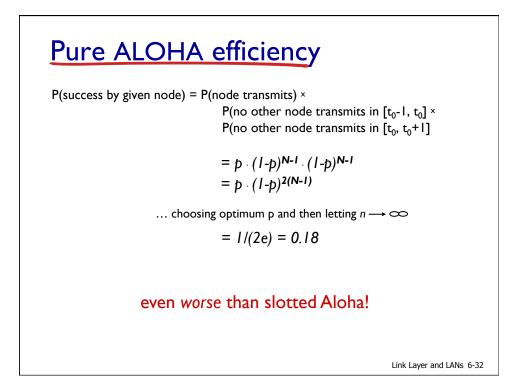
- when node obtains fresh frame, transmits in next slot
  - *if no collision:* node can send new frame in next slot
  - if collision: node retransmits frame in each subsequent slot with probability p until success

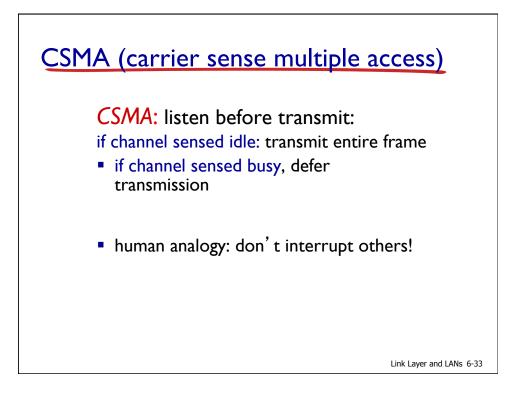


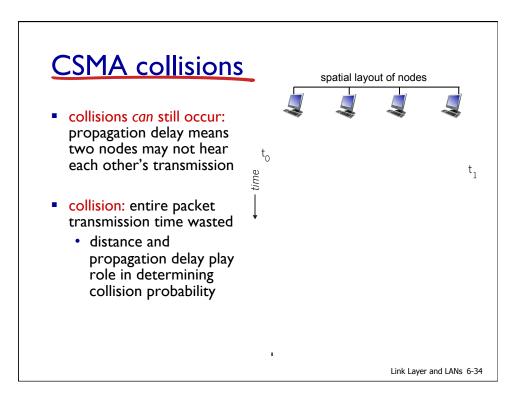


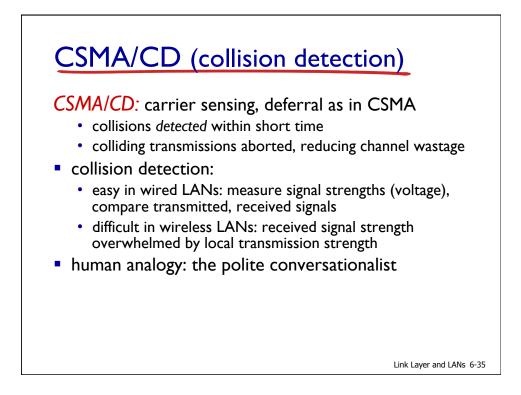


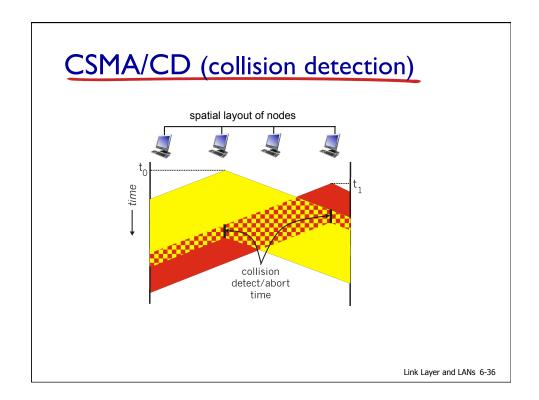












## Ethernet CSMA/CD algorithm

- I. NIC receives datagram from network layer, creates frame
- 2. If NIC senses channel idle, starts frame transmission. If NIC senses channel busy, waits until channel idle, then transmits.
- 3. If NIC transmits entire frame without detecting another transmission, NIC is done with frame!
- 4. If NIC detects another transmission while transmitting, aborts and sends jam signal

- 5. After aborting, NIC enters binary exponential backoff:
  - after m<sup>th</sup> collision, NIC chooses K at random from {0, 1, 2, ..., 2<sup>m</sup>-1}. NIC waits K x 512 bit times, returns to Step 2
  - longer backoff interval with more collisions
  - max m = 10
  - bit time = I/R

