

Dept. of Computer Science and Engineering
CSE4210 – Architecture and Hardware for DSP
Exercise Set 1

1. Consider Q5.2 fixed point representation using 2's complement. Multiply 3.25 by -2.5 (show the details of your work)
2. What is the value of the Floating point number represented in hex as 52:3A:00:00 (IEEE754 Format)
3. Problem 1 Chapter 2 in the textbook – 2.1
4. Problem 2.3
5. Problem 2.9
6. Problem 3.1
7. Problem 3.3
8. Problem 3.8