

Dept. of Computer Science and Engineering
CSE3201 – Digital Logic Design
HW 2
Due Oct 26th 2011

From the text book

1. 2.22
2. 2.28
3. 3.2
4. 3.4
5. 3.7
6. 3.15 (a) do this problem using the tabular method
7. 3.21
8. 3.31 (a) and (e)
9. 3.35