

Dept. of Computer Science and Engineering  
CSE3201 – Digital Logic Design  
HW 1  
Due Mon 3<sup>rd</sup> 2011

From the text book

1. 1.13
2. 1.14
3. 1.15
4. 1.16
5. 1.19
6. 1.33
7. Draw a CMOS circuit to implement the function  $F = AB(C + D) + D'E$  Assuming that we have
  - each variable and its complement as available inputs
  - Only variables (A,B,C,D) as available input