

York University  
Dept. of Computer Science and Engineering  
CSE3201  
Digital Logic Design

HW 2  
Due Oct 28<sup>th</sup>, 2009

Form the textbook

1. 4.1
2. 4.2
3. 4.10 (use Q-M method for this problem)
4. 4.27
5. 4.33
6. 4.34
7. 4.38
8. 4.43