

York University
Dept. of Computer Science and Engineering
CSE3201
Project
Fall 2008

Objective:

The objectives of this project is to get you some experience with designing moderate complexity digital systems. You are asked to come up with an idea, discuss it with me, design the circuit, implement it and finally test it.

Guidelines

The project is worth 10% of your final mark. The difficulty of the project should also be proportional to that. The final system should have some inputs and outputs. It is preferable that the inputs come from outside the board, the output could be the LCD, 7-segement display, or some external device.

Proposal

After checking with me if your idea is worth the 10% dedicated to the project, then write and submit your proposal. The proposal should be 1-2 pages in length. It should describe your idea, and outline what you will do.

Demo

You are asked to demonstrate your project in the last lab session of the year.