

York University
Dept. of Computer Science and Engineering
CSE3201
Digital Logic Design

HW 1

Due Oct 10, 2008

Form the textbook

1. 1.16
1. Subtract 78 from 52 using 2's complement and octal (radix complement for octal)
2. 1.21
3. Draw a CMOS circuit to implement the following function $F=A(B+C)+DE$
4. 2.8
5. 2.17
6. 3.5(a)
7. 3.15(a)