York University
Dept. of Computer Science and Engineering
Digital Logic Design
CSE3201
Lab 6

Objective
In this lab, you will design a more complicated circuit that consists of more than one part.

Problem
Design a simple calculator that can multiply two single digit numbers, each digit is 0-9 and entered as a 4-bit binary number. The result is displayed on the 7-segment display. If any of the 2 numbers is greater than 9, the 7-segment display should be set to “E”

Prep work
Draw the circuit, and write the Verilog code. The circuit and the code should be **typed**

In the lab
Implement the circuit and demonstrate it to the TA