

York University
Dept. of Computer Science and Engineering
CSE3201
Digital Logic Design
HW 1
Due Sept. 26th 2007

Form the textbook

1. 1.16
2. 1.21
3. Draw a CMOS circuit to implement the following function $F=A(B+C)+DE$
4. 2.8
5. 2.17
6. 2.20