# On the Space Complexity of Set Agreement 

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## Agreement Using Registers

Solving agreement using read/write registers.


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## Parameters

$m$-obstruction-free

| obstruction-free | $m$ |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | 2 | wait-free |  |  |  |

An algorithm is $m$-obstruction-free if some process is guaranteed to terminate when at most $m$ processes continue to take steps.

## Parameters

$m$-obstruction-free $k$-set agreement

| obstruction-free | $m$ |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | 2 | 3 | $n-2$ | $n-1$ | $n$ |

consensus 1
$k$-set agreement: processes must output at most $k$ different values.

$$
n-2
$$

set-
agreement ${ }^{n-1}$

## Parameters

$m$-obstruction-free $k$-set agreement for $n$ processes


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## Space Complexity: Known Results

Problem: $m$-obstruction-free $k$-set agreement for $n$ processes ( $m \leq k<n$ )

How many registers are needed?
Previous work

- $n$ (single-writer) registers are sufficient
- For $m=k=1, \Omega(\sqrt{n})$ registers needed [FHS98]
- For $m=1,2 n-2 k$ registers are sufficient [DFGR13]


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## Repeated Agreement

## Repeated $k$-set agreement problem

- Series $A_{1}, A_{2}, A_{3}, \ldots$ of set agreement instances
- In each instance $A_{i}$, processes output at most $k$ different values
- Processes access instances in order


## Motivation

- Herlihy's universal construction (with $k=1$ ).
- Possible route to lower bound for one-shot problem.


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## Our Results

Bounds on number of registers needed for $m$-obstruction-free $k$-set agreement for $n$ processes

|  | Repeated | One-Shot |
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| Non- | $\geq n+m-k$ | $\geq 2$ [DFGR13] |
| Anon. | $\leq n+2 m-k$ | $\leq n+2 m-k$ |
|  | $n+m-k$ (known ids) | $\leq n+m-k$ (known ids) |
| Anon. | $\geq n+m-k$ | $\geq \sqrt{m\left(\frac{n}{k}-2\right)}$ |
|  | $\leq(m+1)(n-k)+m^{2}+1$ | $\leq(m+1)(n-k)+m^{2}$ |

## - Bounds show dependence on $k$ and $m$

- First anonymous set agreement algorithm
- $\Omega(\sqrt{n})$ lower bound when $m=k=1$ is a special case
- Bounds are nearly tight for repeated (non-anonymous)


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| obstruction-free |  |  |  | $m$ |  |  | wait-free |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | 1 | 2 | 3 |  | $n-2$ | $n-1$ | $n$ |  |
| consensus 1 | $\begin{aligned} & \geq n \\ & \leq n+1 \end{aligned}$ | $x$ | $x$ | -•• | $x$ | $x$ | $x$ |  |
| 2 | $\begin{aligned} & \geq n-1 \\ & \leq n \end{aligned}$ | $\begin{aligned} & \geq n \\ & \leq n+2 \end{aligned}$ | $x$ | -•• | $x$ | $x$ | $x$ |  |
| 3 | $\geq n-2$ <br> $\leq n-1$ | $\geq n-1$ $\leq n+1$ | $\begin{aligned} & \geq n \\ & \leq n+3 \end{aligned}$ | -•• | $x$ | $x$ | $x$ |  |
| $k \quad 4$ | $\geq n-3$ <br> $\leq n-2$ | $\geq n-2$ $\leq n$ | $\geq n-1$ $\leq n+2$ | -•• | $x$ | $x$ | $x$ |  |
|  | : | : | : | $\bullet$ • | $!$ | : | $\vdots$ |  |
| $n-2$ | $\geq 3$ $\leq 4$ | $\geq 4$ $\leq 6$ | $\geq 5$ $\leq 8$ | -• | $\begin{aligned} & \geq n \\ & \leq 2 n-2 \end{aligned}$ | $x$ | $x$ |  |
| setagreement ${ }^{n-1}$ | $\begin{aligned} & \geq 2 \\ & \leq 3 \end{aligned}$ | $\begin{aligned} & \geq 3 \\ & \leq 5 \end{aligned}$ | $\begin{aligned} & \geq 4 \\ & \leq 7 \end{aligned}$ | -•• | $\left\lvert\, \begin{aligned} & \geq n-1 \\ & \leq 2 n-3\end{aligned}\right.$ | $\begin{aligned} & \geq n \\ & \leq 2 n-1 \end{aligned}$ |  | YORK |

## Repeated Set Agreement Bounds

Repeated $m$-obstruction-free $k$-set agreement for $n$ processes

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| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | 1 | 2 | 3 |  | $n-2$ | $n-1$ |  | $n$ |
| consensus 1 | $\geq n$ $\leq n$ | $x$ | $x$ | -•• | $x$ | $x$ | $\times$ | $x$ |
| 2 | $\begin{aligned} & \geq n-1 \\ & \leq n \end{aligned}$ | $\begin{aligned} & \geq n \\ & \leq n \end{aligned}$ | $x$ | $\cdots$ | $x$ | $x$ |  | $x$ |
| 3 | $\geq n-2$ <br> $\leq n-1$ | $\begin{aligned} & \geq n-1 \\ & \leq n \end{aligned}$ | $\begin{aligned} & \geq n \\ & \leq n \end{aligned}$ | -•• | $x$ | $x$ | $x$ | $x$ |
| $k \quad 4$ | $\geq n-3$ <br> $\leq n-2$ | $\geq n-2$ $\leq n$ | 俍 $\begin{aligned} & \geq n-1 \\ & \leq n\end{aligned}$ | -•• | $x$ | $x$ | $x$ | $x$ |
|  | : | : | : | $\bullet$ • | : | : |  | : |
| $n-2$ | $\geq 3$ $\leq 4$ | $\geq 4$ $\leq 6$ | $\geq 5$ $\leq 8$ | -•• | $\geq n$ $\leq n$ | $x$ |  | $x$ |
| setagreement $^{n-1}$ | $\begin{aligned} & \geq 2 \\ & \leq 3 \end{aligned}$ | $\begin{aligned} & \geq 3 \\ & \leq 5 \end{aligned}$ | $\begin{aligned} & \geq 4 \\ & \leq 7 \end{aligned}$ | -•• | $\geq n-1$ $\leq n$ | $\begin{aligned} & \geq n \\ & \leq n \end{aligned}$ |  | $\times$ YOR |

## Lower Bound for Repeated Set Agreement

Consider any $m$-obstruction-free $k$-set agreement algorithm.
Construct an execution:

every continuation
by $Q_{1}$ writes only to $A_{1}$
every continuation
by $Q_{2}$ writes only to $A_{2}$
$Q_{i}$ 's are disjoint sets of $m$ processes each.
$P_{i}$ is set of processes disjoint from $Q_{1}, Q_{2}, \ldots, Q_{i}$.
$A_{i}$ is a set of registers.

## Constructing the Execution

$$
\begin{aligned}
& Q=\left\{p_{1}, p_{2}, p_{3}, \ldots, p_{m}\right\} \\
& P=\{ \} \\
& A=\{ \}
\end{aligned}
$$

## Constructing the Execution

$Q=\left\{p_{1}, p_{2}, p_{3}, \ldots, p_{m}\right\}$
$P=\{ \}$
A $=\{ \}$
Oncos
Let processes in $Q$
run until a process
$p_{1}$ is poised to
write to $R_{1} \notin A$

## Constructing the Execution

$Q=\left\{\quad p_{2}, p_{3}, \ldots, p_{m}, p_{m+1}\right\}$
$P=\left\{p_{1}\right\}$
$A=\left\{R_{1}\right\}$
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$Q=\left\{\quad p_{2}, p_{3}, \ldots, p_{m}, p_{m+1}\right\}$
$P=\left\{p_{1}\right\}$
$A=\left\{R_{1}\right\}$
 run until a process run until a process
$p_{1}$ is poised to $\quad p_{2}$ is poised to write to $R_{1} \notin A \quad$ write to $R_{2} \notin A$

## Constructing the Execution

$Q=\left\{\quad p_{3}, \ldots, p_{m}, p_{m+1}, p_{m+2}\right\}$
$P=\left\{p_{1}, p_{2}\right\}$
$A=\left\{R_{1}, R_{2}\right\}$


Let processes in $Q$ Let processes in $Q$ run until a process run until a process
$p_{1}$ is poised to $\quad p_{2}$ is poised to write to $R_{1} \notin A \quad$ write to $R_{2} \notin A$

## Constructing the Execution

$Q=\left\{\quad p_{3}, \ldots, p_{m}, p_{m+1}, p_{m+2}\right\}$
$P=\left\{p_{1}, p_{2}\right\}$
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Let processes in $Q$ Let processes in $Q$
run until a process run until a process
$p_{1}$ is poised to write to $R_{1} \notin A$
$p_{2}$ is poised to
write to $R_{2} \notin A$

Repeat until every continuation by $Q$ writes only registers in $A$.

## Lower Bound for Repeated Set Agreement

$Q_{i}$ 's are disjoint sets of $m$ processes each.
$P_{i}$ is set of processes disjoint from $Q_{1}, Q_{2}, \ldots, Q_{i}$.
Let $r$ be the number of registers used by the algorithm.


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Let $c=\#$ set agreement instances accessed in this execution.

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$\frac{k+1}{m}$ repetitions yields a contradiction.
This is possible if $n \geq\left(\frac{k+1}{m}-1\right) \cdot m+r=k+1-m+r$.
Thus, $n<k+1-m+r$.
$\Rightarrow r \geq n+m-k$.

## Algorithm for m－Obstruction－Free $k$－Set Agreement

Use snapshot object $A$ with $n+2 m-k$ components．
Repeat：
（1）write（pref，id）into $A[i]$
（2）scan $A$
（3）if at most $m$ different pairs，output value from one that appears twice
（4）if my pair appears only where I last wrote it AND some other pair（ $v, i d^{\prime}$ ）appears twice then pref $\leftarrow v$


## Algorithm for m-Obstruction-Free $k$-Set Agreement

Use snapshot object $A$ with $n+2 m-k$ components.
Repeat:
(1) write (pref, id) into $A[i]$
(2) $\operatorname{scan} A$
(3) if at most $m$ different pairs, output value from one that appears twice
(4) if my pair appears only where I last wrote it AND some other pair ( $v, i d^{\prime}$ ) appears twice then pref $\leftarrow v$
(5) else $i++$

## Example

$$
n=5, m=3, k=4
$$

Use $n+2 m-k=7$ components.

| $v_{5}, 5$ | $v_{3}, 3$ | $v_{2}, 2$ | $v_{2}, 2$ | $v_{2}, 2$ | $v_{3}, 3$ | $v_{5}, 5$ |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |

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$$
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Use $n+2 m-k=7$ components.


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Use $n+2 m-k=7$ components.


## Validity

Validity: Every output value is the input of some process.
Trivial proof: values in $A$ are input values of some process.

## Agreement

Agreement：At most $k$ different values are output．
We don＇t care what the first $k-m$ processes output．
Claim：The last $n+m-k$ processes output $\leq m$ values．
When the first of those $n+m-k$ processes does final scan $S$ ，
it sees at most $m$ different pairs．We prove that afterwards，
－Only pairs with those values can appear in 2 locations．
－No other value can ever be output．
Intuition：

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Intuition:

| $n+2 m-k$ |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $v_{5}, 5$ | $v_{3}, 3$ | $v_{2}, 2$ | $v_{2}, 2$ | $v_{2}, 2$ | $v_{3}, 3$ | $v_{5}, 5$ |

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Intuition:


## m-Obstruction-Free Termination

Termination: If at most $m$ processes continue taking steps, one will terminate.

A process stops when it sees at most $m$ different pairs in $A$.
If at most $m$ processes continue taking steps, we prove

- They cannot all stand still (exchanging preferences)
- Eventually only their pairs are stored in $A$
- Two pairs with same id have same value
$\Rightarrow m$-obstruction-free termination


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## Algorithm For Repeated Set Agreement

Main idea：
－Write history of output values for all previous instances， along with id and pref．
－Ignore entries written by processes working on earlier instances．
－If you read value written by a process working on a later instance，adopt its output for your instance．
Can be done using the same number of registers．

## Simpler Algorithm When Ids Are Known



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## Simpler Algorithm When Ids Are Known



## Anonymous Lower Bound

For any set $V$ of $m$ input values
let $\alpha(V)$ be a run of $m$ processes that outputs those $m$ values.
We consider the sequence of registers written (for the first time) in $\alpha(V)$.

Caim
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Yields the $\Omega\left(\sqrt{\frac{m n}{k}}\right)$ 'ower bound.

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We consider the sequence of registers written (for the first time) in $\alpha(V)$.

## Claim

If $r \leq \sqrt{m\left(\frac{n}{k}-2\right)}$ there are infinitely many sets $V$ such that $\alpha(V)$ writes to the same sequence of $r+1$ registers.
Yields the $\Omega\left(\sqrt{\frac{m n}{k}}\right)$ lower bound.

## Proof of Claim

Inductively construct register sequence $\mathbf{R}_{i}$ of length $i$ such that infinitely many $\alpha(V)$＇s register sequences start with $\mathbf{R}_{i}$ ．
$\mathrm{R}_{0}=$
Suppose we have $\mathbf{R}_{i-1}$
Consider V＇s such that $\alpha(V)$＇s register sequence starts with $R_{i-1}$ If there are $\frac{k+1}{m}$ disjoint $V$＇s such that $\alpha(V)$ writes only to $\mathbf{R}_{i-1}$ ， combine them to get run with $k+1$ outputs．Contradiction．

So infinitely many of the $V$＇s have longer register sequence． One register $R$ appears next in infinitely many of the $V$＇s
register sequences．


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So infinitely many of the V's have longer register sequence. One register $R$ appears next in infinitely many of the $V$ 's
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Take $\mathbf{R}_{i}=\mathbf{R}_{i-1} \cdot\langle R\rangle$.

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$\mathbf{R}_{0}=\langle \rangle$.
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Consider V's such that $\alpha(V)$ 's register sequence starts with $\mathbf{R}_{i-1}$. If there are $\frac{k+1}{m}$ disjoint $V$ 's such that $\alpha(V)$ writes only to $\mathbf{R}_{i-1}$, combine them to get run with $k+1$ outputs. Contradiction.
Combined run uses $\Theta\left(\frac{r^{2} k}{m}\right)$ processes,
so can continue this argument as long as $r$ is $O\left(\sqrt{\frac{n m}{k}}\right)$.
So infinitely many of the $V$ 's have longer register sequence.
One register $R$ appears next in infinitely many of the $V$ 's register sequences.
Take $\mathbf{R}_{i}=\mathbf{R}_{i-1} \cdot\langle R\rangle$.

## Recap

Bounds on number of registers needed for $m$-obstruction-free $k$-set agreement for $n$ processes

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|  | $\leq n+m-k$ (known ids) | $\leq n+m-k$ (known ids) |
| Anon. | $\geq n+m-k$ | $\geq \sqrt{m\left(\frac{n}{k}-2\right)}$ |
|  | $\leq(m+1)(n-k)+m^{2}+1$ | $\leq(m+1)(n-k)+m^{2}$ |

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| Non- | $\geq n+m-k$ | $\geq 2$ [DFGR13] |
| Anon. | $\leq n+2 m-k$ | $\leq n+2 m-k$ |
|  | $\leq n+m-k$ (known ids) | $\leq n+m-k$ (known ids) |
| Anon. | $\geq n+m-k$ | $\geq \sqrt{m\left(\frac{n}{k}-2\right)}$ |
|  | $\leq(m+1)(n-k)+m^{2}+1$ | $\leq(m+1)(n-k)+m^{2}$ |

Open Problems: Close the gaps.

