

EECS4315 Mission-Critical Systems

Tutorials on TLA+

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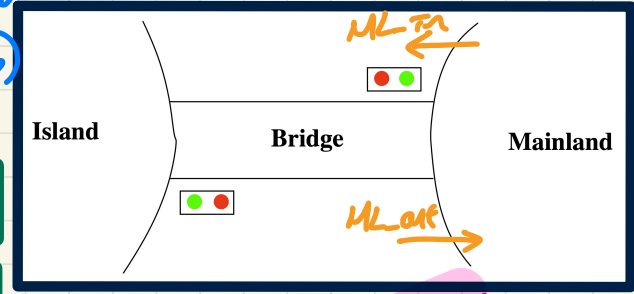
Created: Winter 2023

Bridge Controller: Requirements Document

*E-descriptions
(document the environment
working constraints)*

- ENV1 The system is equipped with two traffic lights with two colors: green and red.
- ENV2 The traffic lights control the entrance to the bridge at both ends of it.
- ENV3 Cars are not supposed to pass on a red traffic light, only on a green one.
- ENV4 The system is equipped with four sensors with two states: on or off.
- ENV5 The sensors are used to detect the presence of a car entering or leaving the bridge: "on" means that a car is willing to enter the bridge or to leave it.

- REQ1 The system is controlling cars on a bridge connecting the mainland to an island.
- REQ2 The number of cars on bridge and island is limited.
- REQ3 The bridge is one-way or the other, not both at the same time.

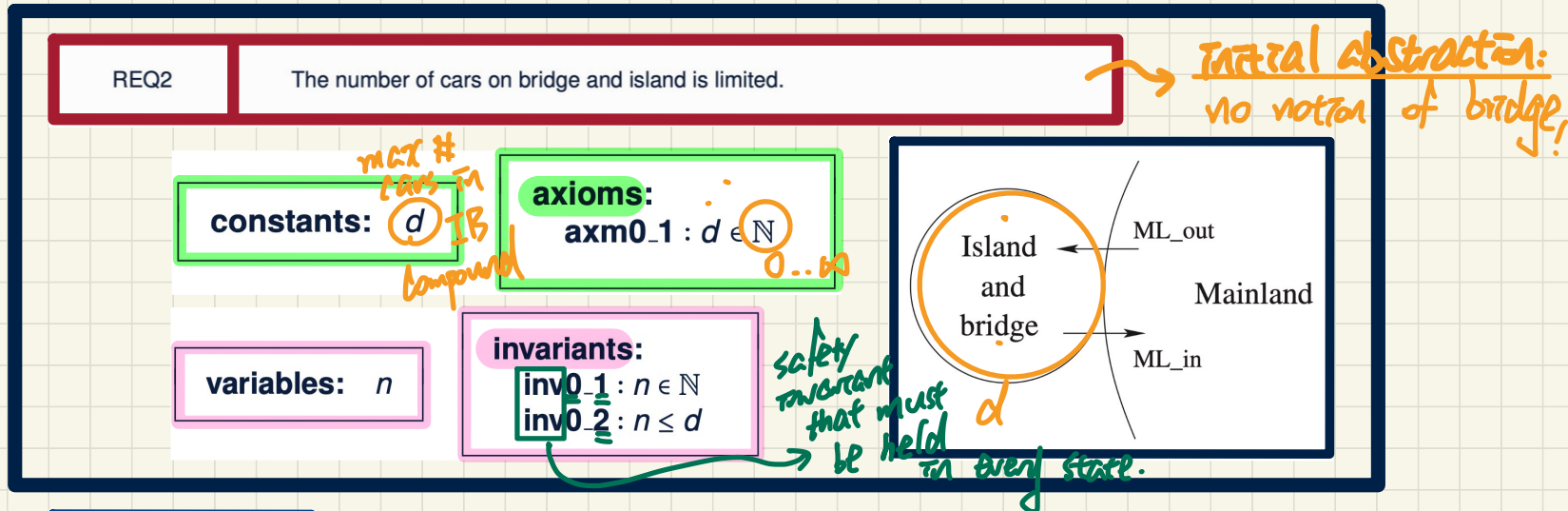


Correct by construction

*↳ - do not encode all details in a single model
- distribute variables & properties in gradual refinement models*

*R-descriptions
(document the intended functions)*

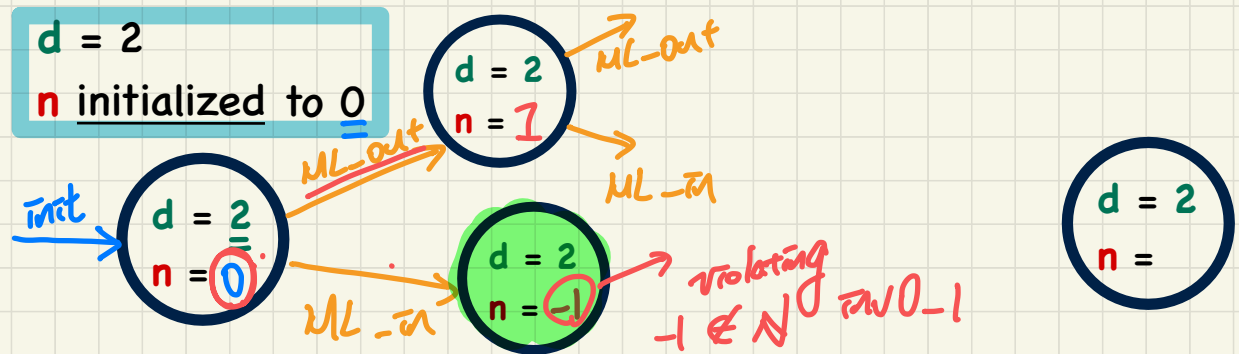
Bridge Controller: State Transitions of the Initial Model



ML_out begin
implicitly: guard is
 $n := n + 1$
 end

ML_in begin
 $n := n - 1$
 end

State Transition Diagram on an Example Configuration



Logic

P, Q → r
operands

P ^ Q ^ r

P
Q
r

^
^

operators = # operands - 1

TLA+

^ P
^ Q
^ r

operators = # operands

Event Execution via a Central Controller

To model check, system under verification should be kept finite.

```

/* main program
{
  # iterations: bound
  while (i < bound) {
    /* Use the choice operator to simulate the selection of event execution by some central controller.
    (*
    When multiple conditions are satisfied, it is non-deterministic as to which branch would be executed.
    *)
    either {
      if(TRUE) {
        call ML_out();
      };
    } or {
      if(TRUE) {
        call ML_in();
      };
    };
    i := i + 1;
  }
}
    
```

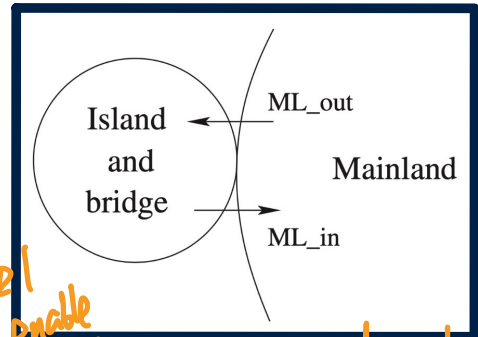
a constant which will be instantiated differently when creating "models"

design-level constraint

if (...) else if (...)

3 iterations

non-deterministic choice
 $\bar{i} = 0$
 $\bar{i} = 1$
 $\bar{i} = 2$

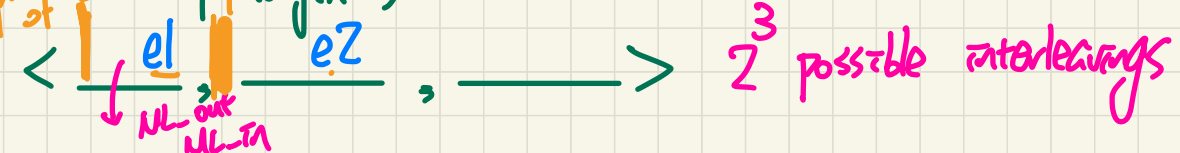


for this part of interleaving to be valid, the post-state of e1 must enable e2 (i.e., e2's guard evaluates to T)

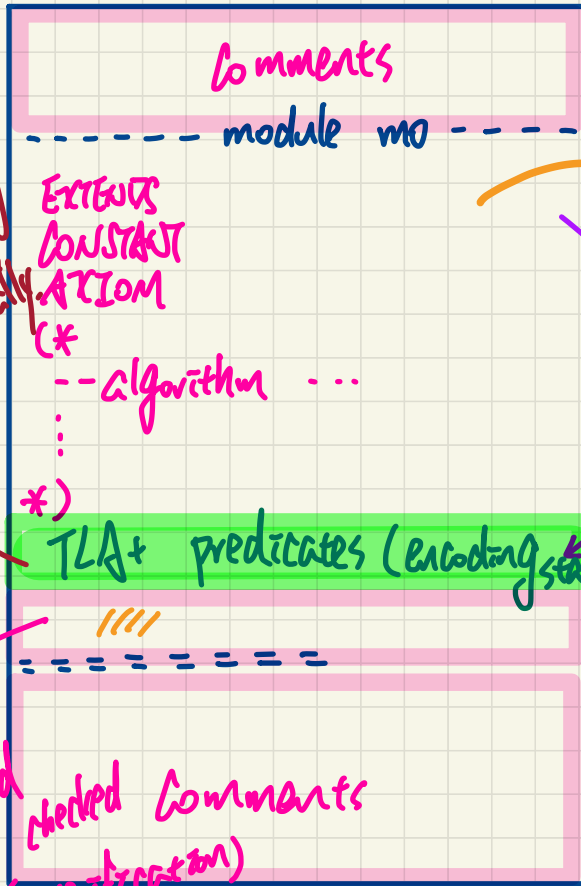
at the same time, the choice of execution is e.g. bound = 3

interleaving of sys. events of length 3

System Trace: sequence of event occurrences



Using TLA+ **Manual** vs. **Automated**



no. tla
(module)

Re-translation unnecessary if (1) the properties part modified (2) algo. part unchanged.

Never modify part(s) of this manually

(1) Model checking cannot be performed directly upon PlusCal

(2) Translate the PlusCal algorithm ^{into} (TLA+ predicates) whenever before-after predicate

an intermediate version of algo. is done (so as to find errors)

syntax, types.

boolean properties (to be added to TLC for verification)

checked Comments

PlusCal vs. TLA+

LHS: label names

```

--algorithm bridgeController_m0 {
  variable n = 0, i = 0;

  procedure ML_out() {
    ML_out_action: n := n + 1;
    return;
  }

  procedure ML_in() {
    ML_in_action: n := n - 1;
    return;
  }

  /* main program
  {
    loop: while (i < bound) {
      choice: either {
        ML_out_guard: if(TRUE) {
          * ML_out_occurs: call ML_out();
        };
      }
      or {
        ML_in_guard: if(TRUE) {
          ML_in_occurs: call ML_in();
        };
      };
      progress: i := i + 1
    }
  }
  
```

PC: program counter

location for which prog exec is about to start

```

VARIABLES n, i, pc, stack → call. Prog.
vars == << n, i, pc, stack >>

Init == (* Global variables *)
  /\ n = 0
  /\ i = 0
  /\ stack = << >>
  /\ pc = "loop"

ML_out_action == /\ pc = "ML_out_action"
  /\ n' = n + 1
  /\ pc' = Head(stack).pc
  /\ stack' = Tail(stack)
  /\ i' = i

ML_in == ML_out_action

ML_in_action == /\ pc = "ML_in_action"
  /\ n' = n - 1
  /\ pc' = Head(stack).pc
  /\ stack' = Tail(stack)
  /\ i' = i

ML_in == ML_in_action
  
```

keep track of prog. exec.

PRE-state vs. post-state
 PC Translation (control flow)

```

loop == /\ pc = "loop"
  /\ IF i < bound
  THEN /\ pc' = "choice"
  ELSE /\ pc' = "Done"
  /\ UNCHANGED << n, i, stack >>

choice == /\ pc = "choice"
  /\ \/\ /\ pc' = "ML_out_guard"
  \/\ \/\ pc' = "ML_in_guard"
  /\ UNCHANGED << n, i, stack >>

ML_out_guard == /\ pc = "ML_out_guard"
  /\ IF TRUE
  THEN /\ pc' = "ML_out_occurs"
  ELSE /\ pc' = "progress"
  /\ UNCHANGED << n, i, stack >>

ML_out_occurs == /\ pc = "ML_out_occurs"
  /\ stack' = << [ procedure |-> "ML_out"
  /\ pc |-> "progress" ] >>
  /\ pc' = "ML_out_action"
  /\ UNCHANGED << n, i >>

ML_in_guard == /\ pc = "ML_in_guard"
  /\ IF TRUE
  THEN /\ pc' = "ML_in_occurs"
  ELSE /\ pc' = "progress"
  /\ UNCHANGED << n, i, stack >>

ML_in_occurs == /\ pc = "ML_in_occurs"
  /\ stack' = << [ procedure |-> "ML_in",
  pc |-> "progress" ] >>
  \/\ stack
  /\ pc' = "ML_in_action"
  /\ UNCHANGED << n, i >>

progress == /\ pc = "progress"
  /\ i' = i + 1
  /\ pc' = "loop"
  /\ UNCHANGED << n, stack >>
  (* Allow infinite stuttering to prevent deadlock on termination. *)
  Terminating == pc = "Done" /\ UNCHANGED vars

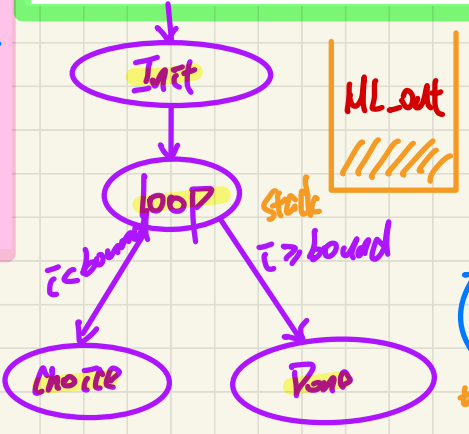
Next == ML_out \/ ML_in \/ loop \/ choice \/ ML_out_guard \/ ML_out_occurs
  \/\ ML_in_guard \/ ML_in_occurs \/ progress
  \/\ Terminating

Spec == Init /\ [] [Next]_vars
  
```



concatenating (push)

where to resume the exec when ML-out returns



what may happen after Init

anything executed disjunction

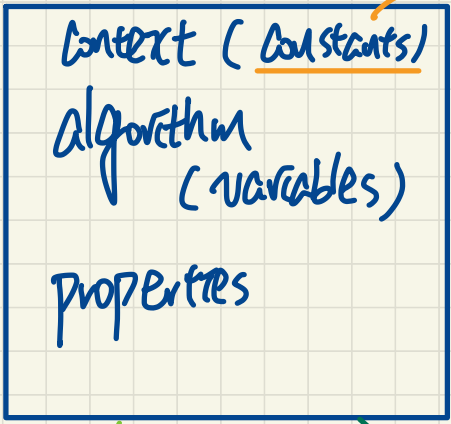
top-level

is defined as

PlusCal

Exercise: Complete the Control-Flow Diagram

module



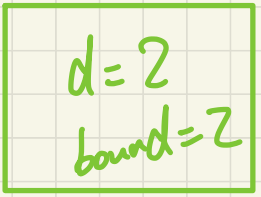
d, bound

should be restricted in order to be feasible for model checking

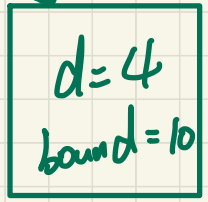
instantiate

instantiate

model 1



model 2

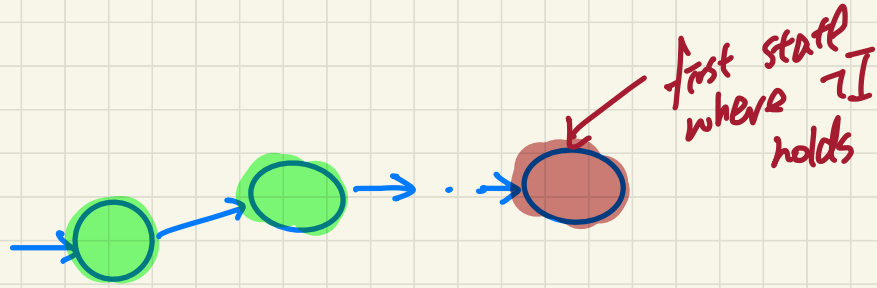


Remotelabs File Transfer

Windows: Ctrl + shift + Alt

Mac: Ctrl + shift + option

Error Trace



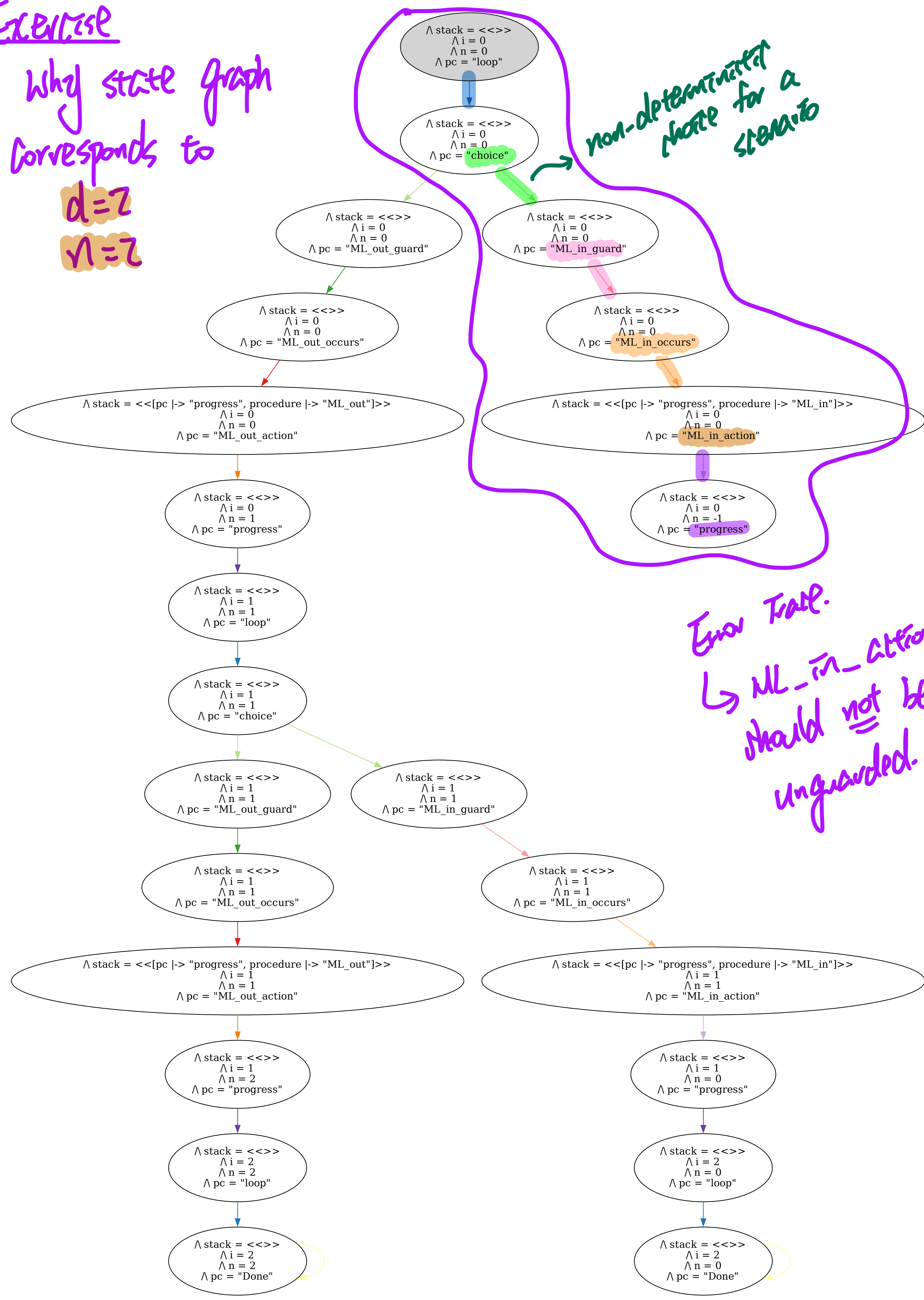
In checking that invariant property I holds on each state, if it fails, it implies that there's one system state s.t. $\neg I$ holds. An error trace shows how to get to this state from init.

Using an error trace and the state graph, debug by finding the problematic part of the model/module.

Exercise

Why state graph corresponds to $d=2$ $n=2$

$d=2$
 $n=2$



Next State Actions						
ML_out_occurs	ML_out_guard	ML_out_action	choice	progress	ML_in_guard	ML_in_action
			loop		ML_in_occurs	Terminating

```

<<
[
  i |-> 0,
  n |-> 0,
  pc |-> "loop",
  stack |-> <<>>
],
[
  i |-> 0,
  n |-> 0,
  pc |-> "choice",
  stack |-> <<>>
],
[
  i |-> 0,
  n |-> 0,
  pc |-> "ML_in_guard",
  stack |-> <<>>
],
[
  i |-> 0,
  n |-> 0,
  pc |-> "ML_in_occurs",
  stack |-> <<>>
],
[
  i |-> 0,
  n |-> 0,
  pc |-> "ML_in_action",
  stack |-> <<[pc |-> "progress", procedure |-> "ML_in"]>>
],
[
  i |-> 0,
  n |-> -1,
  pc |-> "progress"
],
[
  i |-> 0,
  n |-> 0,
  pc |-> "ML_in_guard",
  stack |-> <<>>
],
[
  i |-> 0,
  n |-> 0,
  pc |-> "ML_in_occurs",
  stack |-> <<>>
],
[
  i |-> 0,
  n |-> 0,
  pc |-> "ML_in_action",
  stack |-> <<[pc |-> "progress", procedure |-> "ML_in"]>>
],
[
  i |-> 0,
  n |-> 0,
  pc |-> "progress"
],
[
  i |-> 0,
  n |-> 0,
  pc |-> "loop"
],
[
  i |-> 0,
  n |-> 0,
  pc |-> "loop"
],
[
  i |-> 0,
  n |-> 0,
  pc |-> "Done"
],
[
  i |-> 0,
  n |-> 0,
  pc |-> "Done"
]
>>
  
```

```

--algorithm bridgeController_m0 {
  variable n = 0, i = 0;

  procedure ML_out() {
    ML_out_action: n := n + 1;
    return;
  }

  procedure ML_in() {
    ML_in_action: n := n - 1;
    return;
  }

  /* main program
  {
    loop: while (i < bound) {
      choice: either {
        ML_out_guard: if(TRUE) {
          ML_out_occurs: call ML_out();
        };
      } or {
        ML_in_guard: if(TRUE) {
          ML_in_occurs: call ML_in();
        };
      };
      progress: i := i + 1;
    }
  }
  
```

Error Trap.
ML_in_action should not be unguarded.

pre-state value of n before ML_in-action takes effect

post-state value -1 ∈ N ≡ False where the value violates 0 ≤ n ∈ N

more restrictive!

1. Add `inv0_2` to TLC checker for invariant checking.

2. If it fails, study the **error trace** & **state graph**

↳ fix the model accordingly

↳ Re-translate PlusCal

↳ Re-run TLC checker

practice more
by injecting
different errors
to algorithm, and
see how error traces
can indicate fixes

3. Update the `ML-out-guard` if necessary

↳ add `deadlock_free` to TLC checker for invariant checking.